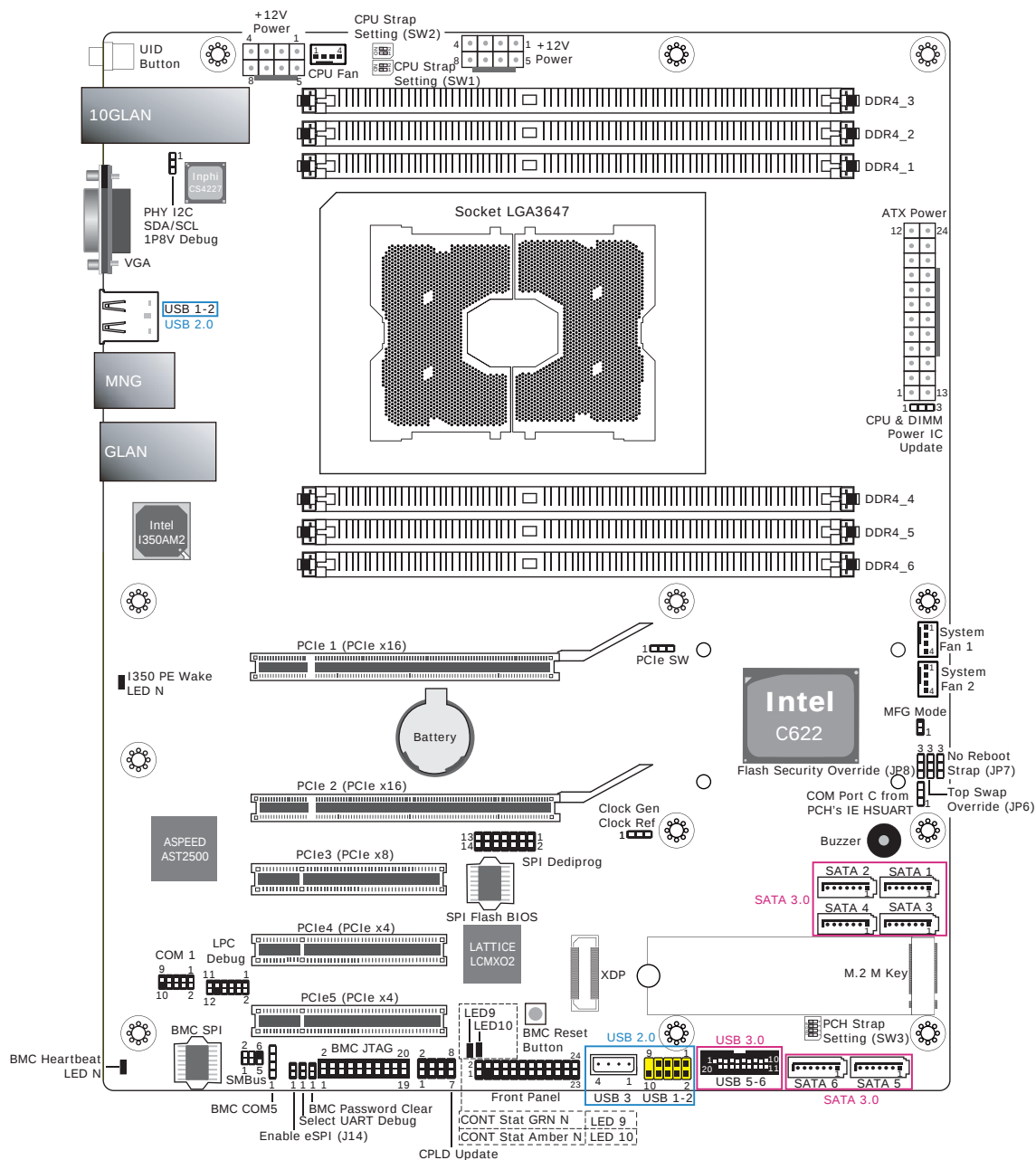


DFI®

PL610-C622



Top Swap Override	JP6
Disable Weak Internal Pull-down	1-2 On
Enable Top Swap Mode (default)	2-3 On

No Reboot Strap	JP7
Disable Weak Internal Pull-down	1-2 On
Enable (default)	2-3 On

Flash Security Override	JP8
Disable (default)	1-2 On
Enable	2-3 On

PCIE SW	JP14
RISER4 (default)	1-2 On
10G	2-3 On

Enable eSPI	J14
LPC Mode (default)	0
eSPI Mode	1

Select UART Debug	J15
UART1 as BMC Console Port (default)	0
UART5 as BMC Console Port	1

Battery Notice

CPU Strap Setting	SW1 Pin1	PCH Strap Setting	SW3 Pin1
TXT Agent Disable (default)	Off	BIOS Recovery Mode Disable (default)	Off
TXT Agent Enable	On	BIOS Recovery Mode Enable	On

CPU Strap Setting	SW1 Pin2	PCH Strap Setting	SW3 Pin2
Safe Mode Boot Disable (default)	Off	Password Clear Disable (default)	Off
Safe Mode Boot Enable	On	Password Clear Enable	On

CPU Strap Setting	SW2 Pin1	PCH Strap Setting	SW3 Pin3
TXT PLTEN Disable	Off	ME Firmware Update Disable (default)	Off
TXT PLTEN Enable (default)	On	ME Firmware Update Enable	On

CPU Strap Setting	SW2 Pin2	PCH Strap Setting	SW3 Pin4
BIST Disable (default)	Off	Normal (default)	Off
BIST Enable	On	Clear CMOS Data	On



Battery Usage

The lithium ion battery powers the real-time clock and CMOS memory. It is an auxiliary source of power when the main power is shut off.



Safety Measures

- Danger of explosion if battery incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to local ordinance.



Mesure de Sécurité de l'usage de Batterie

Batterie:

- Danger d'explosion si la batterie n'est pas correctement remplacée.
- Remplacez seulement avec le même type ou équivalent recommandé par le fabricant.
- Traitez des batteries usées selon le règlement local.