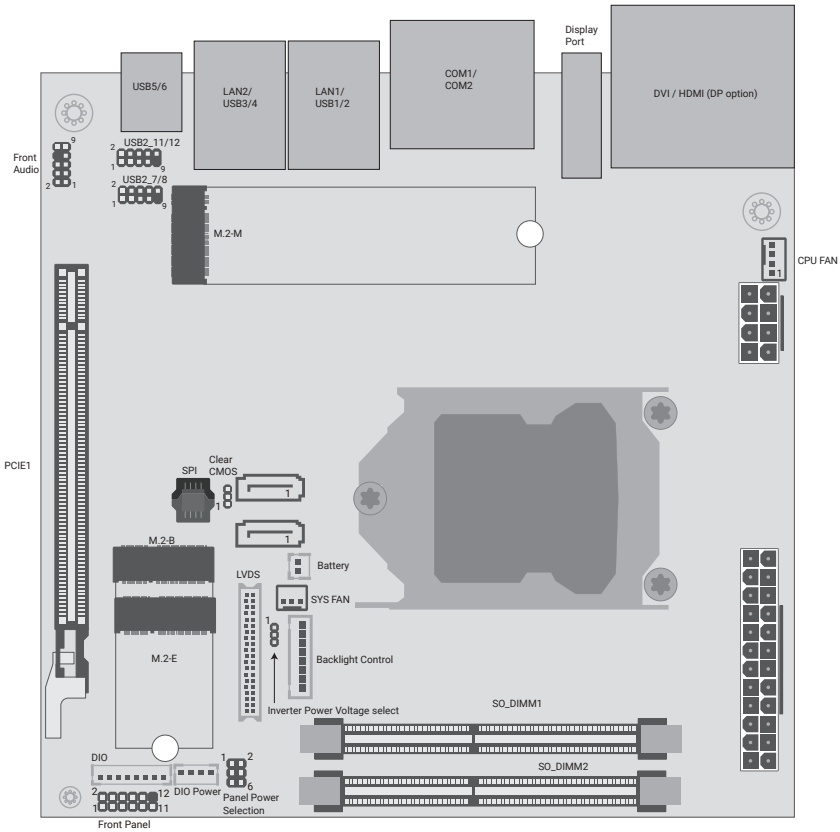


Board Layout - Top



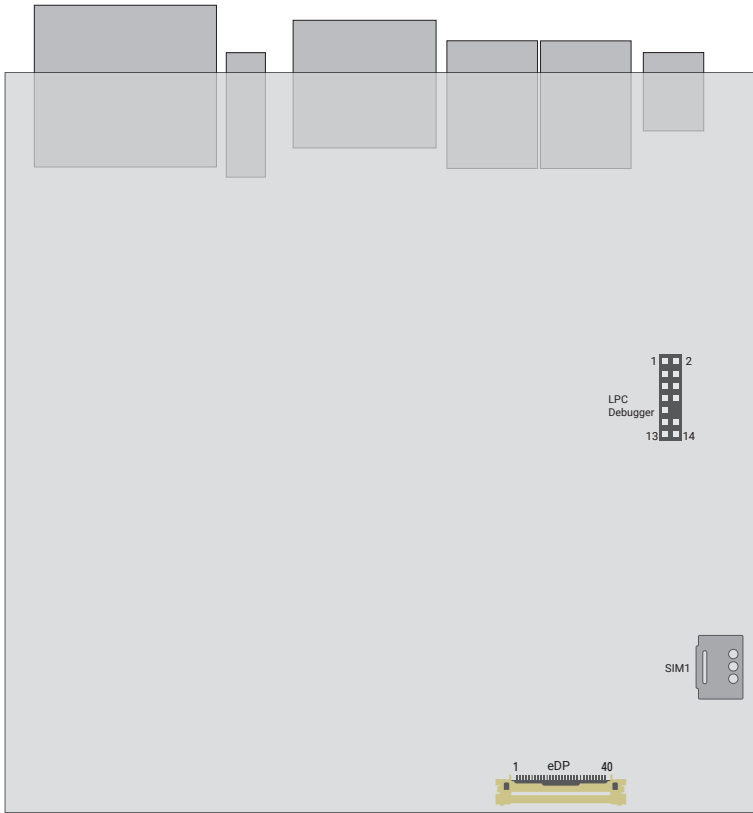
Jumpers

Panel Power Selection	
12V	1-2 On
5V	3-4 On
3.3V(Default)	5-6 On

Clear CMOS	
PCH_RTCRST-	1-2 On
GND	3-4 On

Inverter Power Voltage Select	
+12V(Default)	1-2 On
5V	2-3 On

Board Layout - Bottom



Pin Assignments

Front Audio

Pin	Assignment	Pin	Assignment
1	MIC2-L	2	GND
3	MIC2-R	4	--
5	LINE2-R	6	MIC2-JD
7	GND	8	--
9	LINE2-L	10	LINE2-JD

USB2_11/12

Pin	Assignment	Pin	Assignment
1	SBV4	2	SBV4
3	USBP_C_11N	4	USBP_C_12N
5	USBP_C_11P	6	USBP_C_12P
7	GND	8	GND
9	--	10	--

USB2_7/8

Pin	Assignment	Pin	Assignment
1	SBV4	2	SBV4
3	USBP_C_7N	4	USBP_C_8N
5	USBP_C_7P	6	USBP_C_8P
7	GND	8	GND
9	--	10	--

CPU FAN

Pin	Assignment
1	GND
2	+12V
3	CPUFANIN
4	CPUFANOUT

SYS FAN

Pin	Assignment
1	GND
2	SYSFANOUT1
3	SYSFANIN1

Battery

Pin	Assignment	Pin	Assignment
1	+3.3V	2	GND

Backlight Control

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	DIMMING	4	PANEL_PWR
5	LVDS_3V3	6	BLONOFF
7	VCC_INV_PWR	8	VCC_INV_PWR

DIO Power

Pin	Assignment	Pin	Assignment
1	+12V	2	GND
3	5VSB	4	5V

DIO

Pin	Assignment	Pin	Assignment
1	D_IOA0_C	2	D_IOA1_C
3	D_IOA2_C	4	D_IOA3_C
5	D_IOA4_C	6	D_IOA5_C
7	D_IOA6_C	8	D_IOA7_C

Front Panel

Pin	Function	Assignment	Function	Pin	Assignment
1		--		2	V_SUS_LED
3	HDD LED	3V3	SYS LED	4	V_SUS_LED
5		HD_LED		6	SUSLED
7	RESET	GND	POWER	8	GND
9		SYS_RST-		10	PWR_BTN-

LPC Debugger

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	FRAME#	6	VCC3
7	LAD3	8	GND
9	LAD2	10	--
11	SERIRQ	12	GND
13	5VSB	14	5V

eDP

Pin	Assignment	Pin	Assignment
1	--	2	VCC_INV_PWR
3	VCC_INV_PWR	4	VCC_INV_PWR
5	VCC_INV_PWR	6	--
7	--	8	DIMMING 27
9	BLONOFF 27	10	eDP_GND
11	eDP_GND	12	eDP_GND
13	eDP_GND	14	eDP_HPDI_B_R
15	GND	16	GND
17	GND	18	GND
19	--	20	PANEL_PWR
21	PANEL_PWR	22	PANEL_PWR
23	PANEL_PWR	24	GND
25	eDP_AUXN_C	26	eDP_AUXP_C
27	GND	28	eDP_LANE0_P
29	eDP_LANE0_N	30	GND
31	eDP_LANE1_P	32	eDP_LANE1_N
33	GND	34	eDP_LANE2_P
35	eDP_LANE2_N	36	GND
37	eDP_LANE3_P	38	eDP_LANE3_N
39	GND	40	--