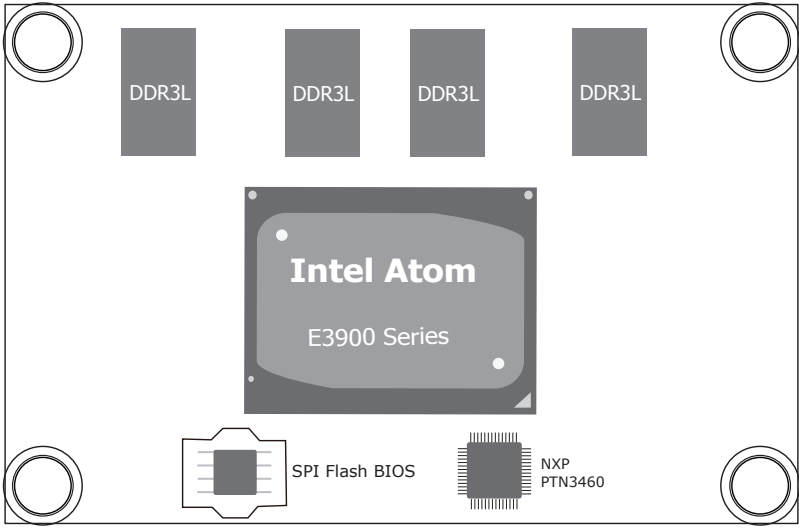
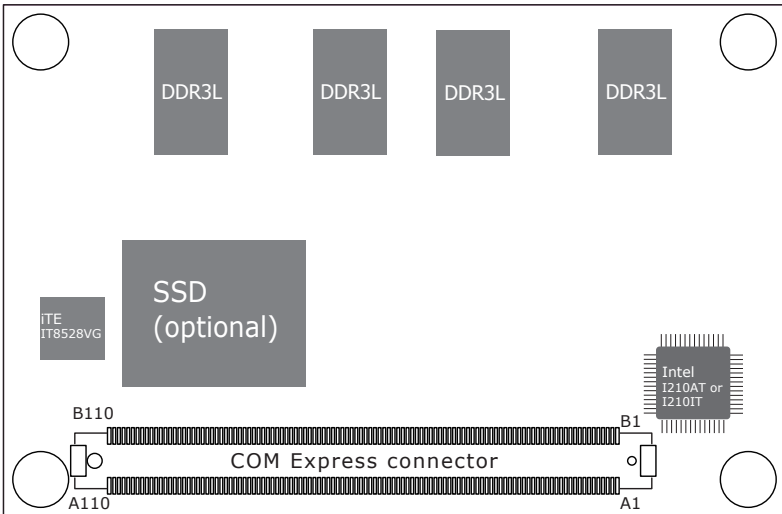


Board Layout and COM Express Pin Assignments



TOP



BOTTOM

Row A	Row B
A1 GND	B1 GND
A2 GBE_MDI3-	B2 GBE_ACT# / 3.3V Suspend
A3 GBE_MDI3+	B3 LPC_FRAME#
A4 GBE_LED_100- / 3.3V Suspend	B4 LPC_AD0
A5 GBE_LED_1000- / 3.3V Suspend	B5 LPC_AD1
A6 GBE_MDI2-	B6 LPC_AD2
A7 GBE_MDI2+	B7 LPC_AD3
A8 GBE_LED_LINK- / 3.3V Suspend	B8 LPC_DRQ0#
A9 GBE_MDI1-	B9 LPC_DRQ1#
A10 GBE_MDI1+	B10 LPC_CLK
A11 GND	B11 GND
A12 GBE_MDI0-	B12 PWRBTN# / 3.3V Suspend
A13 GBE_MDI0+	B13 SMB_CK / 3.3V Suspend
A14 NA	B14 SMB_DAT / 3.3V Suspend
A15 SUS_S3#	B15 SMB_ALERT# / 3.3V Suspend
A16 SATA0_TX+	B16 SATA1_TX+
A17 SATA0_TX-	B17 SATA1_TX-
A18 SUS_S4#	B18 SUS_STAT#
A19 SATA0_RX+	B19 SATA1_RX+
A20 SATA0_RX-	B20 SATA1_RX-
A21 GND	B21 GND
A22 USB_SSRX0-	B22 USB_SSTX0-
A23 USB_SSRX0+	B23 USB_SSTX0+
A24 SUS_S5#	B24 PWR_OK
A25 USB_SSRX1-	B25 USB_SSTX1-
A26 USB_SSRX1+	B26 USB_SSTX1+
A27 BATLOW# / Pull up 4.7kohm to 3.3V Suspend	B27 WDT / Pull up 10k ohm to 3.3V
A28 ATA_ACT# / Pull up 10kohm to 3.3V Suspend	B28 NA
A29 AC/HDA_SYNC / 3.3V Suspend	B29 NA
A30 AC/HDA_RST# / 3.3V Suspend	B30 AC/HDA_SDINO
A31 GND	B31 GND
A32 AC/HDA_BITCLK / 3.3V Suspend	B32 SPKR
A33 AC/HDA_SDOUT / 3.3V Suspend	B33 I2C_CK / 3.3V Suspend
A34 BIOS_DIS0#	B34 I2C_DAT / 3.3V Suspend
A35 THRMTRIP# / 3.3V Suspend	B35 THRM#
A36 USB6-	B36 USB7-
A37 USB6+	B37 USB7+
A38 USB_6_7_OC# / 3.3V Suspend	B38 USB_4_5_OC# / 3.3V Suspend

Row A	Row B
A39 USB4-	B39 USB5-
A40 USB4+	B40 USB5+
A41 GND	B41 GND
A42 USB2-	B42 USB3-
A43 USB2+	B43 USB3+
A44 USB_2_3_OC# / 3.3V Suspend	B44 USB_0_1_OC# / 3.3V Suspend
A45 USB0-	B45 USB1-
A46 USB0+	B46 USB1+
A47 VCC_RTC	B47 EXCD1_PERST#
A48 EXCD0_PERST#	B48 EXCD1_CPPE#
A49 EXCD0_CPPE#	B49 SYS_RESET# / Pull up to 3.3V Suspend
A50 LPC_SERIRQ	B50 CB_RESET# / Pull up to 3.3V Suspend
A51 GND	B51 GND
A52 NC (Option I2C_CLK_EC)	B52 NC (Option COMe_GPI5)
A53 NC (Option I2C_DATA_EC)	B53 NC (Option COMe_GPO5)
A54 GPIO	B54 GPO1
A55 NC (Option COMe_GPI4)	B55 NC (Option COMe_GPI6)
A56 NC (Option COMe_GPO4)	B56 NC (Option COMe_GPO6)
A57 GND	B57 GPO2
A58 PCIE_TX3+	B58 PCIE_RX3+
A59 PCIE_TX3-	B59 PCIE_RX3-
A60 GND	B60 GND
A61 PCIE_TX2+	B61 PCIE_RX2+
A62 PCIE_TX2-	B62 PCIE_RX2-
A63 GPI1	B63 GPO3
A64 PCIE_TX1+	B64 PCIE_RX1+
A65 PCIE_TX1-	B65 PCIE_RX1-
A66 GND	B66 WAKE0#
A67 GPI2	B67 WAKE1#
A68 PCIE_TX0+	B68 PCIE_RX0+
A69 PCIE_TX0-	B69 PCIE_RX0-
A70 GND	B70 GND
A71 LVDS_A0+ / eDP_TX2+ (option)	B71 DDI0_PAIR0+
A72 LVDS_A0- / eDP_TX2- (option)	B72 DDI0_PAIR0-
A73 LVDS_A1+ / eDP_TX1+ (option)	B73 DDI0_PAIR1+
A74 LVDS_A1- / eDP_TX1- (option)	B74 DDI0_PAIR1-
A75 LVDS_A2+ / eDP_TX0+ (option)	B75 DDI0_PAIR2+
A76 LVDS_A2- / eDP_TX0- (option)	B76 DDI0_PAIR2-

Row A	Row B
A77 LVDS_VDD_EN / eDP_VDD_EN (option)	B77 NA
A78 LVDS_A3+	B78 NA
A79 LVDS_A3-	B79 LVDS_BKLT_EN / eDP_BKLT_EN (option)
A80 GND	B80 GND
A81 LVDS_A_CK+ / eDP_TX3+ (option)	B81 DDI0_PAIR3+
A82 LVDS_A_CK- / eDP_TX3- (option)	B82 DDI0_PAIR3-
A83 LVDS_I2C_CK / eDP_AUX+ (option)	B83 LVDS_BKLT_CTRL / eDP_BKLT_CTRL (option)
A84 LVDS_I2C_DAT / eDP_AUX- (option)	B84 VCC_5V_SBY
A85 GPI3	B85 VCC_5V_SBY
A86 NA	B86 VCC_5V_SBY
A87 eDP_HPDP	B87 VCC_5V_SBY
A88 PCIE0_CLK_REF+	B88 BIOS_DIS1#
A89 PCIE0_CLK_REF-	B89 DD0_HPDP
A90 GND	B90 GND
A91 SPI_POWER / 3.3V Suspend	B91 NA
A92 SPI_MISO / 3.3V Suspend	B92 NA
A93 GPO0	B93 NA
A94 SPL_CLK / 3.3V Suspend	B94 NA
A95 SPI_MOSI / 3.3V Suspend	B95 DDI0_DDC_AUX_SEL
A96 NA	B96 NC / USB_HOST_PRSNP 3.3V (option)
A97 TYPE10# / Pull down 47k ohm to GND	B97 SPL_CS# / 3.3V Suspend
A98 SER0_TX	B98 DDI0_CTRLCLK_AUX+
A99 SER0_RX	B99 DDI0_CTRLCLK_AUX-
A100 GND	B100 GND
A101 SER1_TX	B101 FAN_PWMOUT
A102 SER1_RX	B102 FAN_TACHIN
A103 LID#	B103 SLEEP#
A104 VCC	B104 VCC
A105 VCC	B105 VCC
A106 VCC	B106 VCC
A107 VCC	B107 VCC
A108 VCC	B108 VCC
A109 VCC	B109 VCC
A110 GND	B110 GND



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