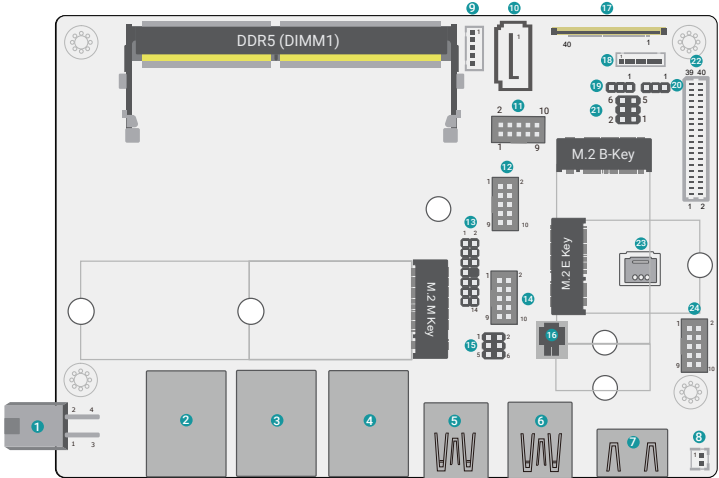
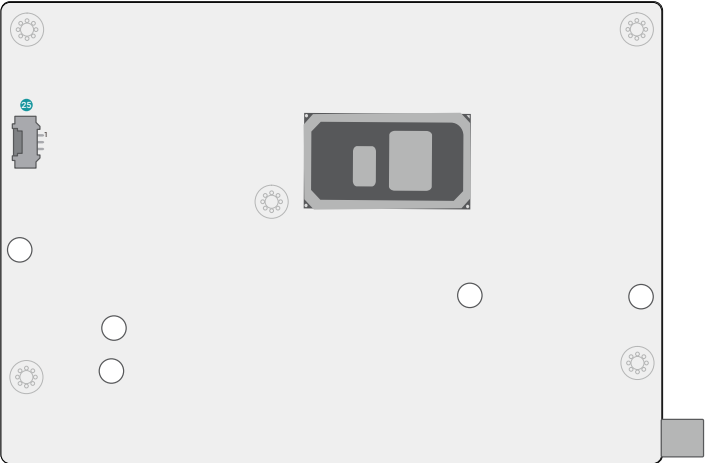


## Board Layout

Top View



Bottom View



1	Power Connector	14	COM1
2	2.5G LAN1	15	Front Panel
3	2.5G LAN2	16	SPI ROM
4	2.5G LAN3	17	eDP
5	USB3.2 Gen2	18	LVDS Inverter Power
6	▲USB3.2 Gen2 ▼USB Type-C	19	Panel Backlight Selection
7	HDMI	20	Panel Inverter/Backlight Power Selection
8	RTC Battery	21	Panel VDD Power Jumper
9	SATA Power	22	LVDS
10	SATA 3.0	23	SIM Card Slot
11	USB2.0 6/7	24	Front Audio
12	DIO	25	System Fan
13	eSPI & UART Debug		



## Jumper Settings

<b>19 Panel Backlight Selection</b>	<b>DPJP1</b>
3.3V (Default)	1-2 On
5V	2-3 On

<b>20 Panel Inverter / Backlight Power Selection</b>	<b>DPJP2</b>
12V (Default)	1-2 On
5V	2-3 On

<b>21 Panel VDD Power Jumper</b>	<b>DPJP3</b>
12V	1-2 On
5V	3-4 On
3.3V (Default)	5-6 On

## PIN Assignment

### 25 ▶ System Fan (J10)

Pin	Assignment
1	TACH
2	VOUT
3	GND

### 1 ▶ Power Connector (CN11)

Pin	Assignment
1	GND
2	GND
3	DC_IN
4	DC_IN

### 9 ▶ SATA Power (J1001)

Pin	Assignment
1	5V
2	5V
3	12V
4	GND
5	GND

### 18 ▶ LVDS Inverter Power (J1002)

Pin	Assignment
1	GND
2	
3	PWM Brightness Control
4	Panel Power
5	+3.3V
6	Backlight On/Off
7	
8	Inverter Power

### 15 ▶ Front Panel (J11)

Pin	Assignment	Pin	Assignment
1	Power Button	2	SUS LED Power
3	GND	4	SUS LED
5	Reset	6	HDD LED

**14 ► COM1 (TSJ1)**

Pin	RS232	RS422	RS485
1	DCD-	TXD-	Data-
2	SIN	TXD+	Data+
3	SOUT	RXD+	NC
4	DTR-	RXD-	NC
5	GND	GND	GND
6	DSR-	NC	NC
7	RTS-	NC	NC
8	CTS-	NC	NC
9	RI-	NC	NC

**12 ► DIO (J1000)**

Pin	Assignment	Pin	Assignment
1	DIO7	2	DIO6
3	DIO5	4	DIO4
5	DIO3	6	DIO2
7	DIO1	8	DIO0
9	5VSB	10	GND

## 24 ▶ Front Audio (AUJ1)

Pin	Assignment	Pin	Assignment
1	Mic-L	2	GND
3	Mic-R	4	NC
5	Line-Out-R	6	Mic-JD
7	GND	8	NC
9	Line-Out-L	10	Line-JD

## 11 ▶ USB2.0 (J3)

Pin	Assignment	Pin	Assignment
1	VCC	2	VCC
3	-DATA	4	-DATA
5	+DATA	6	+DATA
7	GND	8	GND
9	NC	10	NC

## 13 ▶ eSPI & UART Debug (J5)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	ESPI_CLK
3	ESPI_RST#	4	GND
5	ESPI_ALT#	6	UART_TX
7	ESPI_D0	8	--
9	ESPI_D1	10	ESPI_CS
11	ESPI_D2	12	UART_RX
13	ESPI_D3	14	3V3SB

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDSA_Out3+	4	LVDSB_Out3+
5	LVDSA_Out3-	6	LVDSB_Out3-
7	GND	8	GND
9	LVDSA_Out2+	10	LVDSB_Out2+
11	LVDSA_Out2-	12	LVDSB_Out2-
13	GND	14	GND
15	LVDSA_Out1+	16	LVDSB_Out1+
17	LVDSA_Out1-	18	LVDSB_Out1-
19	GND	20	GND
21	LVDSA_Out0+	22	LVDSB_Out0+
23	LVDSA_Out0-	24	LVDSB_Out0-
25	GND	26	GND
27	LVDSA_CLK1+	28	LVDSB_CLK1+
29	LVDSA_CLK1-	30	LVDSB_CLK1-
31	GND	32	GND
33	LVDS_DDC_CLK	34	NC
35	LVDS_DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power

Pin	Assignment	Pin	Assignment
1	NC	21	EDP_VCC_PANEL
2	INV_PWR	22	EDP_VCC_PANEL
3	INV_PWR	23	EDP_VCC_PANEL
4	INV_PWR	24	GND
5	INV_PWR	25	eDPB_AUXN
6	NC	26	eDPB_AUXP
7	NC	27	GND
8	DIMMING_1	28	eDPB_LANE0_C_P
9	BLONOFF_1	29	eDPB_LANE0_C_N
10	eDP_GND	30	GND
11	eDP_GND	31	eDPB_LANE1_C_P
12	eDP_GND	32	eDPB_LANE1_C_N
13	eDP_GND	33	GND
14	eDP_HPDCON_1	34	eDPB_LANE2_C_P
15	GND	35	eDPB_LANE2_C_N
16	GND	36	GND
17	GND	37	eDPB_LANE3_C_P
18	GND	38	eDPB_LANE3_C_N
19	NC	39	GND
20	EDP_VCC_PANEL	40	NC



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

