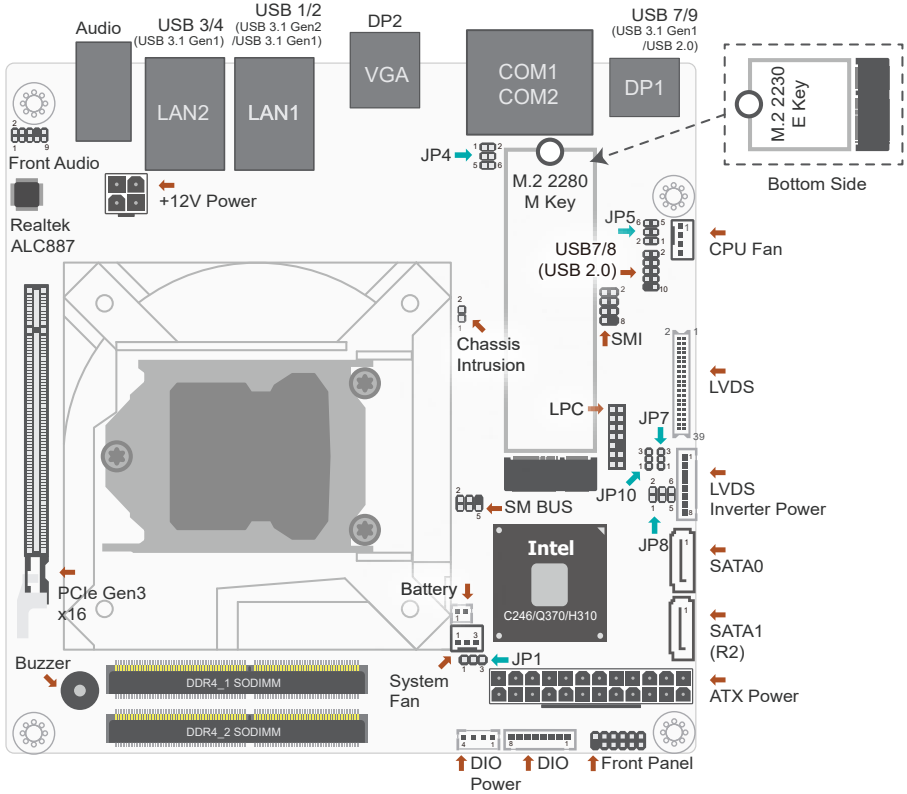


## Board Layout and Jumper Settings



Clear CMOS Data	JP1
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

LVDS Backlight Power	JP7
3.3V (default)	1-2 On
5V	2-3 On

LVDS Panel Power	JP8
12V	1-2 On
5V	3-4 On
3.3V (default)	5-6 On

LVDS Inverter Power	JP10
+12V	1-2 On
5V (default)	2-3 On

COM1/2 Power Selection	JP4/5
RS232 Standard (Default)	1-3 / 2-4 On
RS232 with Power	3-5 / 4-6 On



**Note:**  
COM 1/2 only support RS232.

**Note:**

On H310 rear USB ports 1/2 support USB 3.1 Gen 1 and 7/9 are USB 2.0. The M.2 2280 M key is unavailable on H310.

## Pin Assignments for I/O Connectors

### Chassis Intrusion

Pin	Function	Pin	Function
1	Signal	2	GND

### USB 2.0 Headers (7/8)

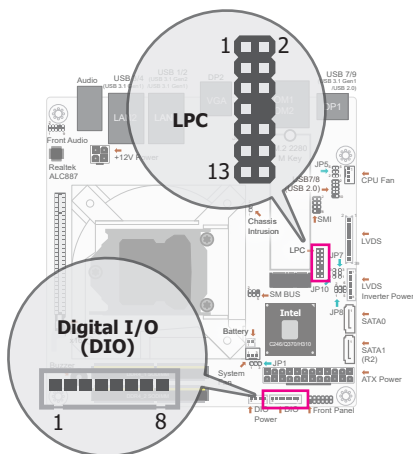
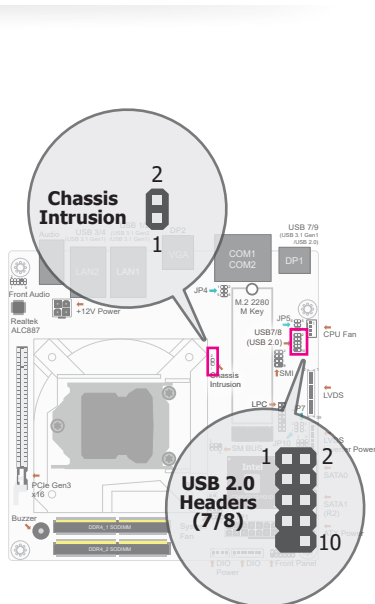
Pin	Assignment	Pin	Assignment
1	VCC	2	VCC
3	Data-	4	Data-
5	Data+	6	Data+
7	GND	8	GND
9	---	10	N.C.

### LPC

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	FRAME#	6	VCC3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5VSB	14	5V

### Digital I/O (DIO)

Pin	Function	Pin	Function
1	DIO_7	5	DIO_3
2	DIO_6	6	DIO_2
3	DIO_5	7	DIO_1
4	DIO_4	8	DIO_0



## Front Panel

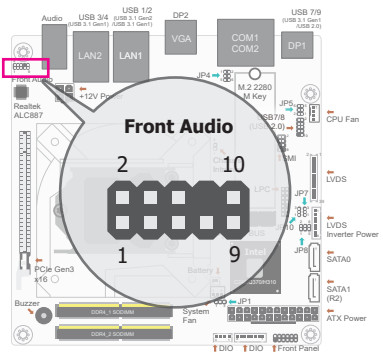
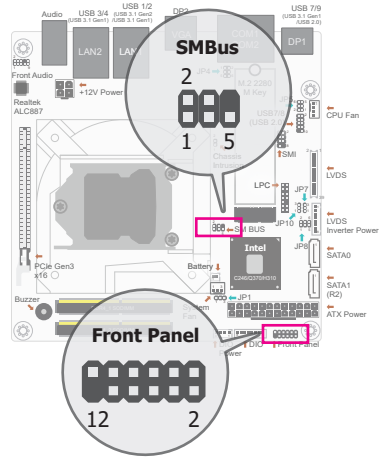
Pin	Assignment	Pin	Assignment
HDD-LED	1 3V3DU	PWR-LED	2 LED Power
	3 SMB_CLK_RESUME		4 LED Power
	5 SMB_ALERT_PCH-		6 Signal
RESET	7 Ground	ATX-SW	8 Ground
	9 Signal		10 Signal
11	N.C.	12	---

## SMBus

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK_RESUME	4	SMB_DATA_RESUME
5	SMB_ALERT_PCH-	6	---

## Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	N.C.
5	Line2-R	6	Mic2-JD
7	GND	8	---
9	Line2-L	10	Line2-JD



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on editions that do not resemble your actual products. For more documentation and drivers, please visit the download page at [go.dfi.com/CS170-Q370/C246](http://go.dfi.com/CS170-Q370/C246), [go.dfi.com/CS170-H310](http://go.dfi.com/CS170-H310), or via the QR codes to the right.



C246/Q370



H310

## LVDS Panel

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDS_A3+	4	LVDS_B3+
5	LVDS_A3-	6	LVDS_B3-
7	GND	8	GND
9	LVDS_A2+	10	LVDS_B2+
11	LVDS_A2-	12	LVDS_B2-
13	GND	14	GND
15	LVDS_A1+	16	LVDS_B1+
17	LVDS_A1-	18	LVDS_B1-
19	GND	20	GND
21	LVDS_A0+	22	LVDS_B0+
23	LVDS_A0-	24	LVDS_B0-
25	GND	26	GND
27	LVDS_CLK1+	28	LVDS_CLK2+
29	LVDS_CLK1-	30	LVDS_CLK2-
31	GND	32	GND
33	LVDS_DDC_CLK	34	NC
35	LVDS_DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power

## LVDS Inverter Power

Pin	Assignment
1	GND
2	GND
3	Panel Inverter Brightness Voltage Control
4	Panel Power
5	+3.3V
6	Panel Backlight On/Off Control
7	12V (default)/5V
8	12V (default)/5V

