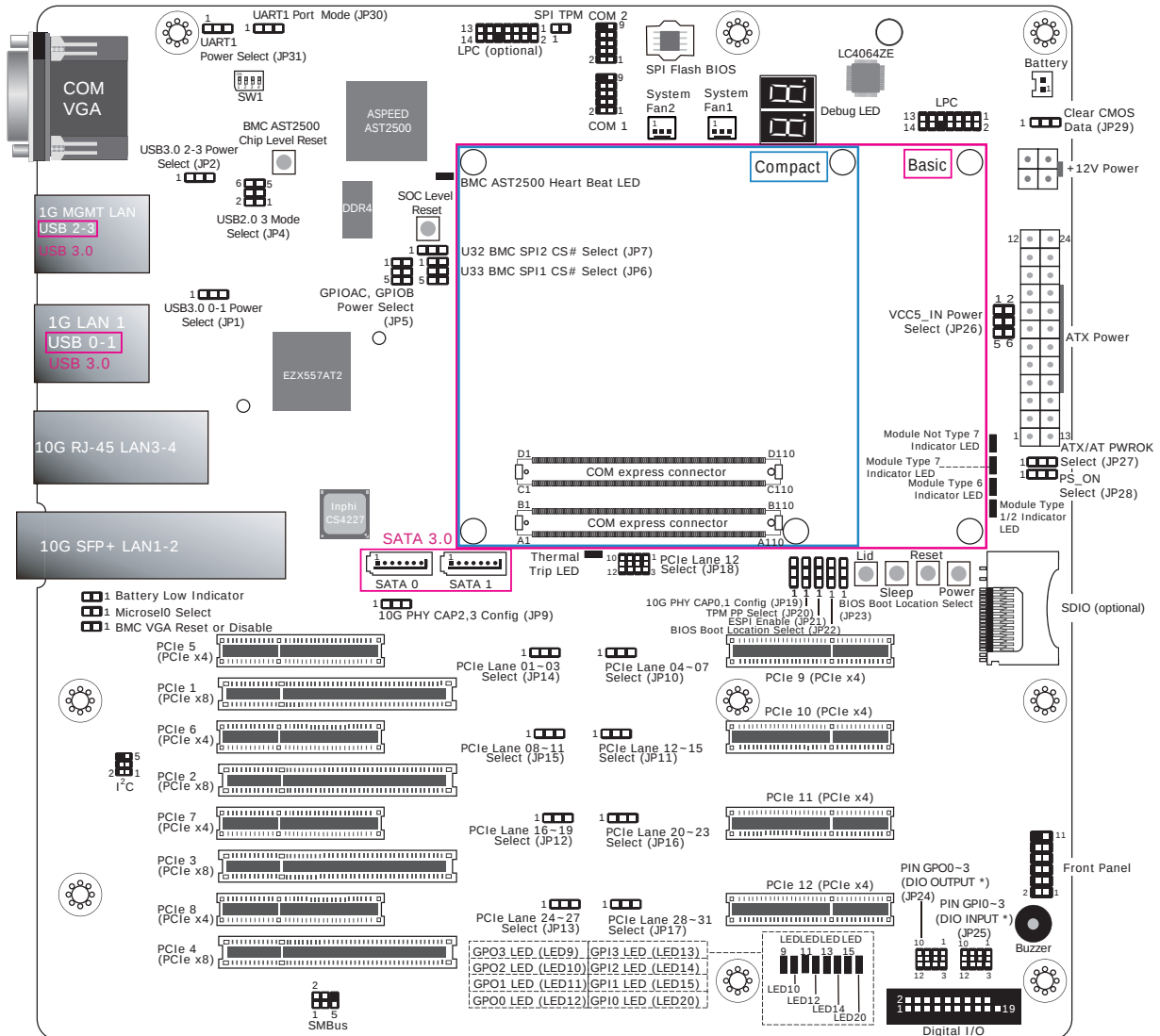


DFI[®]

COM333-I



USB Power Select: 3.0 0-1 (JP1), 3.0 2-3 (JP2)	
+5V	1-2 On
+5V_standby (default)	2-3 On
USB 2.0 3 Mode Select JP4	
USB 2.0 3 Connector	1-3, 2-4 On
USB 2.0 3 BMC (default)	3-5, 4-6 On
GPIOAC, GPIOB Power Select JP5	
+1.8V	1-2 On
+2.5V	3-4 On
+3V_standby (default)	5-6 On

U33 BMC SPI1 CS# Select JP6	
BMC SPI CS0# (default)	1-2 On
BMC SPI CS1#	3-4 On
BMC SPI CS2#	5-6 On
U32 BMC SPI2 CS# Select JP7	
BMC SPI2 CS0#	1-2 On
BMC SPI2 CS1# (default)	2-3 On
10G PHY Config: CAP 0,1 (JP19), CAP 2,3 (JP9)	
MDIO only (default)	1-2 On
MDIO or I2C	2-3 On

PCIe Lane 01~03 Select (JP14)	PCIe Lane 04~07 Select (JP10)	PCIe Lane 08~11 Select (JP15)	PCIe Lane 12~15 Select (JP11)	PCIe Lane 16~19 Select (JP12)	PCIe Lane 20~23 Select (JP16)	PCIe Lane 24~27 Select (JP13)	PCIe Lane 28~31 Select (JP17)	
PCIe5 x4 Slot (default)	PCIe9 x4 Slot (default)	PCIe6 x4 Slot (default)	PCIe10 x4 Slot (default)	PCIe7 x4 Slot (default)	PCIe11 x4 Slot (default)	PCIe8 x4 Slot (default)	PCIe12 x4 Slot (default)	1-2 On
PCIe1 x8 Slot	PCIe1 x8 Slot	PCIe2 x8 Slot	PCIe2 x8 Slot	PCIe3 x8 Slot	PCIe3 x8 Slot	PCIe4 x8 Slot	PCIe4 x8 Slot	2-3 On

TPM_PP on CB Select JP20	
On	1-2 On
Off (default)	2-3 On

ESPI Enable Select JP21	
LPC BUS Enable (default)	1-2 On
ESPI BUS Enable	2-3 On

PCIe LANE 12 Select	JP18	
PCIe L12 to BMC (default)	1-2, 4-5, 7-8, 10-11 On	
PCIe L12 to Multiplexer (Ref JP11)	2-3, 5-6, 8-9, 11-12 On	
BIOS Boot Location Select	JP22	JP23
SPI0 on Module (default)	2-3 On	2-3 On
SPI0 on Carrier	2-3 On	1-2 On
Module SPI Only	1-2 On	1-2 On
PIN GPIO0~3: DIO Output (JP24), DIO Input (JP25)		
LED on (default)	2-3, 5-6, 8-9, 11-12 On	
LED off	1-2, 4-5, 7-8, 10-11 On	
VCC5_IN Power Select	JP26	
+5V_standby (default)	1-2 On	
+5V	3-4 On	
NC	5-6 On	
ATX/AT Mode Select (Power_OK)	JP27	
ATX PS PWROK IN (default)	1-2 On	
By BMC Control	2-3 On	
AT Mode, no PWROK IN	1-X	
PS_ON Select	JP28	
ATX Mode auto PWR_ON (default)	1-2 On	
AT Mode always ON	2-3 On	
Clear CMOS Data	JP29	
Normal (default)	1-2 On	
Clear CMOS data	2-3 On	
UART1 Port Mode	JP30	
SOR	1-2 On	
Debug mode (default)	2-3 On	
UART1 Power Select	JP31	
3V3	1-2 On	
3VDU (default)	2-3 On	
BIOS Boot Select	Switch1 Pin1	Switch1 Pin2
Disable SPI Interface	Off	Off
Enable SPI Master (default)	Off	On
Reserved	On	Off
Enable SPI Pass Through	On	ON
BMC 2nd Boot Watchdog Timer	Switch1 Pin3	
Disable (default)	Off	
Enable	On	
MAC#1 Interface Type	Switch1 Pin4	
RMII/NCSI	Off	
RGMII (default)	On	

Note: JP27 and JP28 need to be set simultaneously.

Battery Notice



Battery Usage

The lithium ion battery powers the real-time clock and CMOS memory. It is an auxiliary source of power when the main power is shut off.



Safety Measures

- Danger of explosion if battery incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to local ordinance.



Mesure de Sécurité de l'usage de Batterie

Batterie:

- Danger d'explosion si la batterie n'est pas correctement remplacée.
- Remplacez seulement avec le même type ou équivalent recommandé par le fabricant.
- Traitez des batteries usées selon le règlement local.