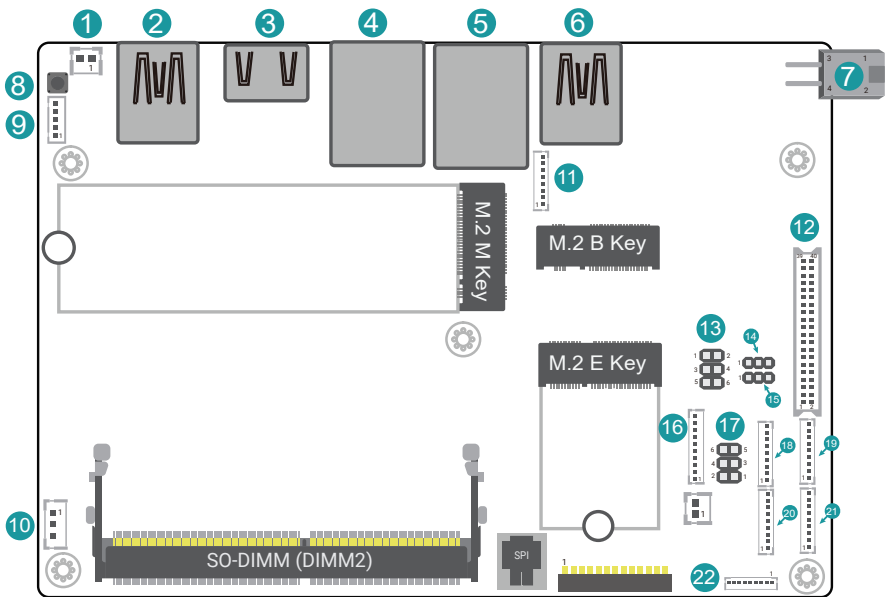
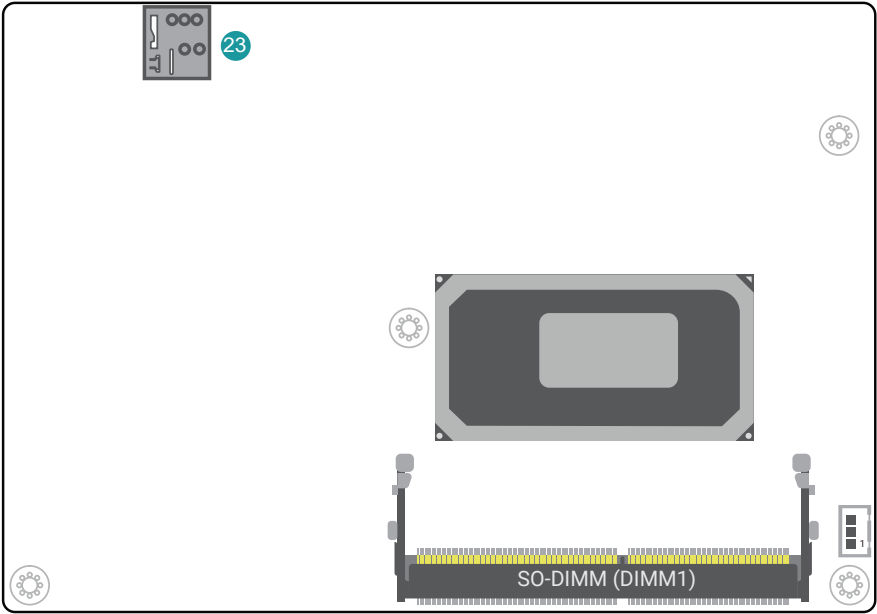


Board Layout

Top View



Bottom View



1	RTC Battery	21	COM3
2	▲USB 3.2 ▼USB Type C	22	Audio
3	HDMI	23	SIM Card Slot
4	LAN2		
5	LAN1		
6	▲USB3_2/1 ▼USB3_2/2		
7	DC IN		
8	Clear CMOS		
9	I2C		
10	System Fan		
11	USB2.0 2x		
12	LVDS		
13	Panel Power		
14	Panel Inverter Power Selection		
15	Panel Backlight Selection		
16	DIO		
17	Front Panel		
18	COM2		
19	COM1		
20	COM4		



Jumper Settings

8 Clear CMOS	SW1
Normal (default)	1-2 Open
Clear CMOS	1-2 Short

15 Panel Backlight Selection	DPJP1
3V3 (default)	1-2 ON
5V	2-3 ON

14 Panel Inverter Power Selection	DPJP2
12V (default)	1-2 ON
5V	2-3 ON

13 Panel Power	DPJP3
12V	1-2 ON
5V	3-4 ON
3V3 (default)	5-6 ON



PIN Assignment

1 ▶ RTC Battery (CN7)

Pin	Assignment
1	RTC power
2	GND

7 ▶ DC IN (CN5)

Pin	Assignment
1	GND
2	GND
3	DC_IN
4	DC_IN

10 ▶ System Fan (CN13)

Pin	Assignment
1	TACT
2	+12V
3	GND

17 ▶ Front Panel (J2)

Pin	Assignment	Pin	Assignment
1	PWR_BTN0	2	3V3SB
3	GND	4	SUS_LED#
5	SYS_RST0	6	HD_LED#

9 ▶ I2C (CN6)

Pin	Assignment
1	3V3SB
2	GND
3	I2C1_SCL_SOC
4	I2C1_SDA_SOC
5	I2C1_INT#

11 ▶ USB2.0 (UBCN4)

Pin	Assignment
1	5V
2	USB2_7_C_N
3	USB2_7_C_P
4	GND
5	5V
6	USB2_9_C_N
7	USB2_9_C_P
8	GND

22 ► Audio (AUCN1)

Pin	Assignment
1	LINE2-L
2	AGND_AUDIO
3	LINE2-R
4	HPOUT-R
5	MIC-JD
6	AGND_AUDIO
7	HPOUT-L
8	LINEOUT-JD

21 ► COM3 (TSCN11)

Pin	Assignment
1	MDCD-
2	MSIN
3	MSOUT
4	MDTR-
5	GND
6	MDSR-
7	MRTS-
8	MCTS-
9	MRI-

16 ► DIO (CN12)

Pin	Assignment
1	DI_3
2	DI_2
3	DI_1
4	DI_0
5	DO_3
6	DO_2
7	DO_1
8	DO_0
9	5VSB
10	GND

20 ► COM4 (TSCN2)

Pin	Assignment
1	MDCD-
2	MSIN
3	MSOUT
4	MDTR-
5	GND
6	MDSR-
7	MRTS-
8	MCTS-
9	MRI-

19 ► COM1 (TSCN1)

Pin	RS232	RS422 (HALF-2W)	RS422 (FULL-4W)	RS485 (HALF-2W)
1	DCD-	N.C	TX-	DATA-
2	SIN	N.C	TX+	DATA+
3	SOUT	RX+	RX+	N.C
4	DTR-	RX-	RX-	N.C
5	GND	N.C	GND	GND
6	DSR-	N.C	N.C	N.C
7	RTS-	N.C	N.C	N.C
8	CTS-	N.C	N.C	N.C
9	RI-	N.C	N.C	N.C
10	N.C	N.C	N.C	N.C

18 ► COM2 (TSCN4)

Pin	RS232	RS422 (HALF-2W)	RS422 (FULL-4W)	RS485 (HALF-2W)
1	DCD-	N.C	TX-	DATA-
2	SIN	N.C	TX+	DATA+
3	SOUT	RX+	RX+	N.C
4	DTR-	RX-	RX-	N.C
5	GND	N.C	GND	GND
6	DSR-	N.C	N.C	N.C
7	RTS-	N.C	N.C	N.C
8	CTS-	N.C	N.C	N.C
9	RI-	N.C	N.C	N.C
10	N.C	N.C	N.C	N.C

12 ► LVDS (DPCN1)

Pin	Assignment	Pin	Assignment
1	INV_PWR	2	INV_PWR
3	INV_PWR	4	INV_PWR
5	INV_PWR	6	GND
7	LVDS_3V3	8	GND
9	VCC_PANEL_PWR	10	VCC_PANEL_PWR
11	LVDS_DDC_CLK	12	LVDS_DDC_DATA
13	DIMMING	14	PVCCEN
15	BLONOFF	16	eDP_HPD_CON
17	LVDSA_LANE0_N	18	LVDSA_LANE0_P
19	LVDSA_LANE1_N	20	LVDSA_LANE1_P
21	LVDSA_LANE2_N	22	LVDSA_LANE2_P
23	LVDSA_CLK_N	24	LVDSA_CLK_P
25	LVDSA_LANE3_N	26	LVDSA_LANE3_P
27	GND	28	GND
29	eDP_LVDSB_LANE0_N	30	eDP_LVDSB_LANE0_P
31	eDP_LVDSB_LANE1_N	32	eDP_LVDSB_LANE1_P
33	LVDSB_LANE2_N	34	LVDSB_LANE2_P
35	eDP_LVDSB_CLK_N	36	eDP_LVDSB_CLK_P
37	LVDSB_LANE3_N	38	LVDSB_LANE3_P
39	GND	40	GND



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

