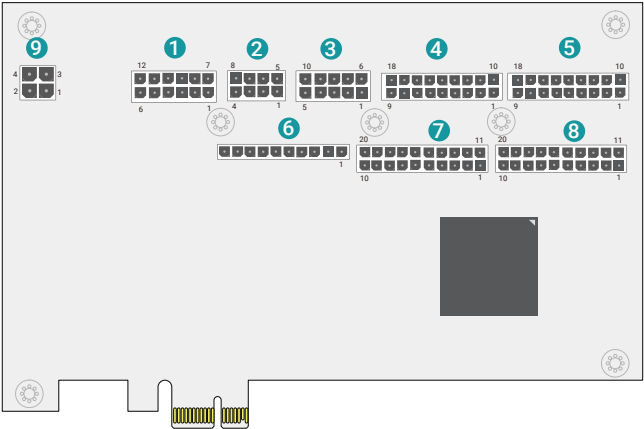
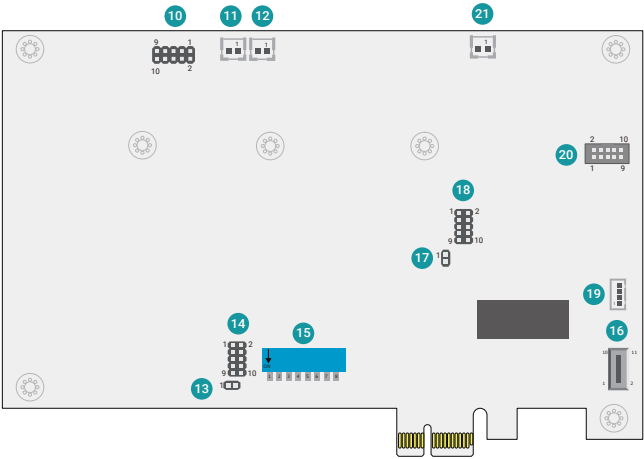


 Board Layout

Top View



Bottom View



1	Door1~6	11	Door8
2	Door8 & I2C	12	Door7
3	COM1/CC-Talk1 COM2/CC-Talk2	13	FPGA Write Protection
4	D01 ~ D016	14	FPGA Write
5	D017~D032	15	8bit DIP Switch
6	Meter Power1~8	16	PClex1 with Power
7	DI1 ~ DI16	17	MCU Write Protection
8	DI17~DI32	18	ST-Link / F.W. updater
9	SPK	19	UART RFU
10	Ext. IO	20	USB/Audio/UART
		21	Coin BAT



Jumper Settings

13	FPGA Write Protection	GFJ1
	F.W. Update Programmable	1-2 On
	Protective Mode	None

17	MCU Write Protection	GMJ1
	F.W. Update Programmable	1-2 On
	Protective Mode	None



PIN Assignment

11 ► Door8 (DRJ8) For External

Pin	Assignment
1	DOORH#2
2	DOORH#1

12 ► Door7 (DRJ7) For System

Pin	Assignment
1	DOORG#2
2	DOORG#1

19 ► UART RFU (TSJ1)

Pin	Assignment
1	+5V (input)
2	HOST_TX (input)
3	HOST_RX (output)
4	GND

21 ► Coin BAT (BTJ1)

Pin	Assignment
1	GND
2	+VBAT

2 ► Door8 & I2C (DRCN2)

Pin	Assignment
1	DOORH#2
2	I2C_ALT# (3.3V)
3	I2C_DAT (3.3V)
4	I2C_CLK (3.3V)
5	DOORH#1
6	+3.3V (~0.5A)
7	GND
8	GND

9 ► SPK (AMCN1)

Pin	Assignment
1	R_SPK+
2	L_SPK+
3	R_SPK-
4	L_SPK-

20 ► USB/Audio/UART (GSJ2)

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	USBD-	4	AUDIO-R
5	USBD+	6	AUDIO-L
7	GND	8	GND
9	UART-RX (Host) (output)	10	UART-TX (Host) (input)

14 ► FPGA Write (GFJ2)

Pin	Assignment	Pin	Assignment
1	JTAG-JTCK	2	GND
3	JTAG-JTDO	4	NC
5	JTAG-JTMS	6	JTAG-3.3V
7	+3.3V	8	JTAG-RST#
9	JTAG-JTDI	10	GND

18 ► ST-Link / F.W. updater (GMJ2) 6 ► Meter Power1~8 (MPCN1)

Pin	Assignment	Pin	Assignment
1	+3.3V (input)	2	UART1_TX
3	GND	4	UART1_RX
5	SWDIO	6	GND
7	SWCLK	8	UART2_TX
9	RST#	10	UART2_RX

Pin	Assignment	Pin	Assignment
1	+12V_MeterPWR1	2	+12V_MeterPWR2
3	+12V_MeterPWR3	4	+12V_MeterPWR4
5	+12V_MeterPWR5	6	+12V_MeterPWR6
7	+12V_MeterPWR7	8	+12V_MeterPWR8
9	NC	10	NC

3 ► COM1/CC-Talk1 COM2/CC-Talk2 (TSCN1)

Pin	Assignment	Pin	Assignment
1	+12V (0.5A)	2	CCTALK_CH2
3	GND	4	COM2_TX
5	COM2_RX	6	+12V (0.5A)
7	CCTALK_CH1	8	GND
9	COM1_TX	10	COM1_RX

10 ► Ext. IO (IBJ1)

Pin	Assignment	Pin	Assignment
1	+12V (~0.1A)	2	IB_DI
3	+5V (~0.1A)	4	IB_DO
5	+3.3V (~0.1A) / GPIO4	6	GPIO1
7	iButton_ Onewire#	8	GPIO2
9	GND	10	GPIO3

4 ► DO1 ~ DO16 (DOCN1)

Pin	Assignment	Pin	Assignment
1	DO_1	2	DO_2
3	DO_3	4	DO_4
5	DO_5	6	DO_6
7	DO_7	8	DO_8
9	DO_9	10	DO_10
11	DO_11	12	DO_12
13	DO_13	14	DO_14
15	DO_15	16	DO_16
17	GND	18	GND

5 ► DO17~DO32 (DOCN2)

Pin	Assignment	Pin	Assignment
1	DO_17 / Meter1	2	DO_18 / Meter2
3	DO_19 / Meter3	4	DO_20 / Meter4
5	DO_21 / Meter5	6	DO_22 / Meter6
7	DO_23 / Meter7	8	DO_24 / Meter8
9	DO_25	10	DO_26
11	DO_27	12	DO_28
13	DO_29	14	DO_30
15	DO_31	16	DO_32
17	GND	18	GND

1 ► Door1~6 (DRCN1)

Pin	Assignment	Pin	Assignment
1	DOORA#2	2	DOORB#2
3	DOORC#2	4	DOORD#2
5	DOORE#2	6	DOORF#2
7	DOORA#1	8	DOORB#1
9	DOORC#1	10	DOORD#1
11	DOORE#1	12	DOORF#1

16 ► PClex1 with Power (GSJ1)

Pin	Assignment	Pin	Assignment
1	GND	2	PCIE_CLK+
3	PCIE_CLK-	4	GND
5	PCIE_RX+	6	PCIE_RX-
7	GND	8	PCIE_TX+
9	PCIE_TX-	10	+12V
11	GND	12	+3.3V
13	+3.3V	14	GND
15	SMB_CLK(reserved)	16	SMB_DATA(reserved)
17	GND	18	+3V3SB
19	PCIE_RST#	20	+12V

7 ► DI1 ~ DI16 (DICN1)

Pin	Assignment	Pin	Assignment
1	DI_1	2	DI_2
3	DI_3	4	DI_4
5	DI_5	6	DI_6
7	DI_7	8	DI_8
9	DI_9	10	DI_10
11	DI_11	12	DI_12
13	DI_13	14	DI_14
15	DI_15	16	DI_16
17	GND	18	GND
19	GND	20	GND

8 ► DI17~DI32 (DICN2)

Pin	Assignment	Pin	Assignment
1	DI_17	2	DI_18
3	DI_19	4	DI_20
5	DI_21	6	DI_22
7	DI_23	8	DI_24
9	DI_25	10	DI_26
11	DI_27	12	DI_28
13	DI_29	14	DI_30
15	DI_31	16	DI_32
17	GND	18	GND
19	GND	20	GND



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

