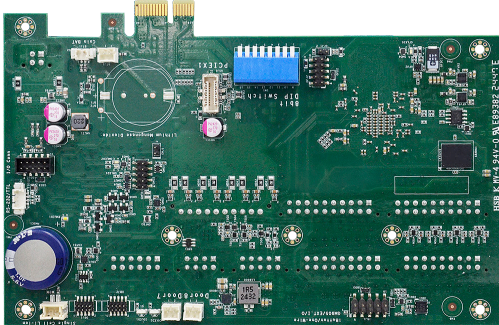


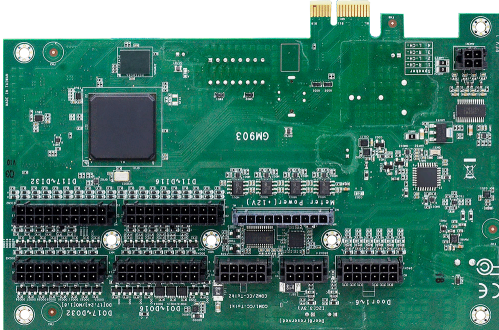
GM903

Gaming I/O control board Peripheral

Gaming I/O control board featuring FPGA and MCU, with 8 intrusion detection channels, battery-backed NVRAM, and reliable data security.



Top View



Bottom View

KEY FEATURES

- FPGA & MCU gaming I/O control board
- Support PCIe1 slot and High/Low speed connectors
- 8 Intrusion detection and battery backed NVRAM
- Reliable Data Security (Secure Boot, Secure Content)

SPECIFICATION

PROCESSOR	FPGA + MCU	FPGA & MUC implementation of the gaming features controller
INTERNAL I/O	Host Interface	High/low speed connector 1 x PCIe x1 slot (Gold finger)
	UART	Pin header for UART signal input
	USB	Pin header for USB 2.0 signal input
	I2C	Pin header for I2C signal input
GAMING I/O	Intrusion Detection	8 x intrusion detection Logs Date/ Time of latest 64 events Operates with and without system active (G3 power from BAT) 5 Years data retention during AC off
	DI/DO	32-bit Digital Input 32-bit OC output (29-bit x 500mA, 3-bit x 3A, up to 24V), Low Active
	Meter Power	8 ports can be set as meter output with disconnect detect
	Serial	2 x RS232/CCTalk (TX/RX)
	Audio	Stereo 6W+6W Class D Audio AMP Output
	Security (by request)	SHA-256 or AES-256 128B RGB
	SRAM (NVRAM)	Support 2pcs SRAM (BGA type) 2 x SRAM supported with battery backup, Default 4MB/Single (up to 16MB/Dual) Backup to Retain Data for 5 years during AC off
	EEPROM	EEPROM 2KB
	Coin battery	1 x CR2032 Lithium 3V/220mAh with socket type 1 header for cable type
	iButton	Support iButton 1-wire protocol Support ds1996 device (default)
POWER	Power input	12V DC in
HARDWARE	Dimension	170mm x 113 mm