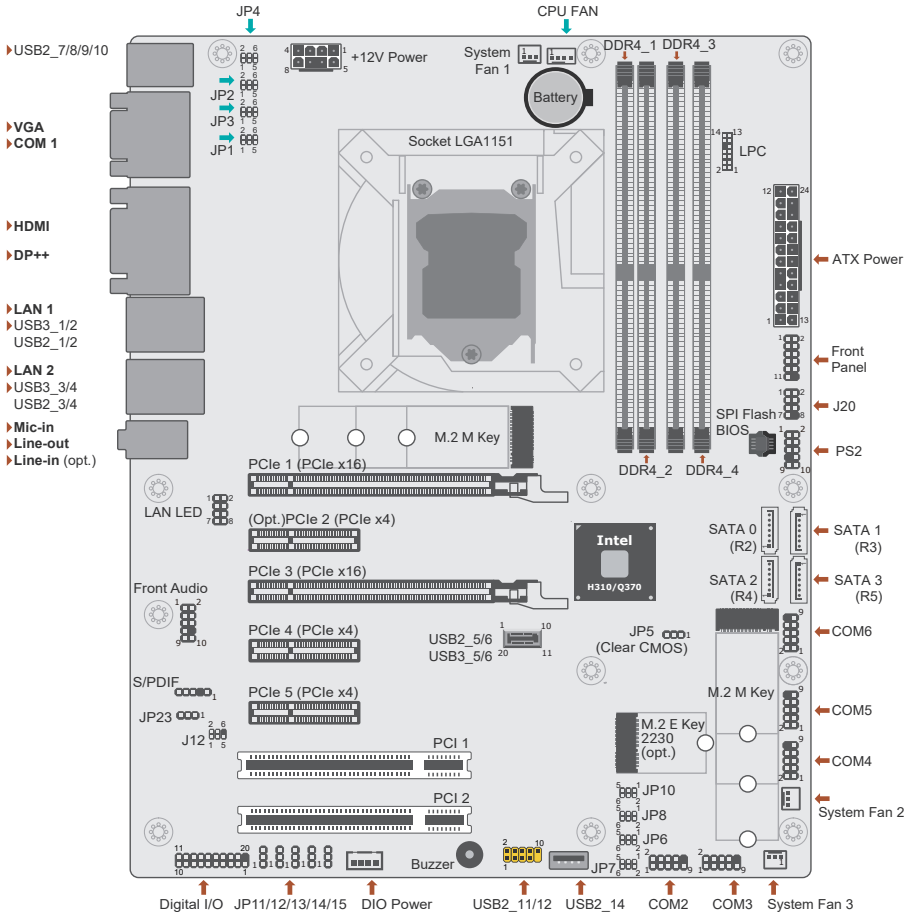


## Board Layout and Jumper Settings



**Note:** PCIe 2 and M.2 E Key are alternatives to each other by BOM selection, the default is PCIe 2.

Clear CMOS Data	JP5
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

U183 Power Select	JP23
+5VDU (default)	1-2 On
+5V	2-3 On

Digital I/O Power select	JP15
+5VSB (default)	1-2 On
+5V	2-3 On

RS232 Power Select	JP4 (COM 1) JP7 (COM 2)
RS232 Standard (default)	1-3, 2-4 On
RS232 with power	3-5, 4-6 On

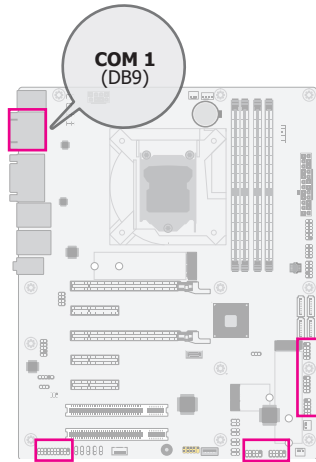
Digital I/O Output	JP11 DIO 0-3 PWR JP12 DIO 4-7 PWR JP13 DIO 8-11 PWR JP14 DIO 12-15 PWR
DIO PWR (default)	1-2 On
GND	2-3 On

COM Mode Select	JP3 (COM1) JP10 (COM2)	JP1/2 (COM 1) JP6/8 (COM 2)
RS232 (default)	1-3, 4-6 On	1-3, 2-4 On
RS422 Full Duplex	3-5, 4-6 On	3-5, 4-6 On
RS485	3-5, 2-4 On	3-5, 4-6 On

## Pin Assignment

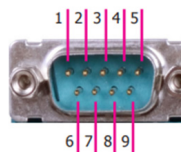
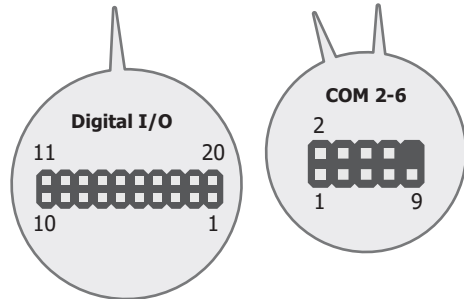
### Digital I/O (DIO)

Pin	Function	Pin	Function
1	GND	11	DIO8
2	GND	12	DIO9
3	DIO7	13	DIO10
4	DIO6	14	DIO11
5	DIO5	15	DIO12
6	DIO4	16	DIO13
7	DIO3	17	DIO14
8	DIO2	18	DIO15
9	DIO1	19	GND
10	DIO0	20	---



### COM Ports (COM 1-6)

Pin	RS232	RS232 with Power	RS422 Full Duplex	RS485
1	DCD-	+12V	RX+	DATA+
2	SIN-	SIN-	RX-	DATA-
3	SO-	SO-	TX+	NC
4	DTR-	DTR-	TX-	NC
5	GND	GND	GND	GND
6	DSR-	DSR-	NC	NC
7	RTS-	RTS-	NC	NC
8	CTS-	CTS-	NC	NC
9	RI-	+5V	NC	NC



**Note:** All three modes, i.e. RS232, RS422, and RS485 are available for COM 1 & 2. Only RS232 is available for COM 3 - 6. RS232 with power is only available for COM 1 & 2.

## Front Panel

Pin	Assignment	Pin	Assignment
1	N.C.	2	V_SUS_LED
3	3V3	4	V_SUS_LED
5	HD_LED	6	SUS_LED
7	Ground	8	Ground
9	SYS_RST	10	PWR_BTN-
11	N.C.	12	---

## S/PDIF

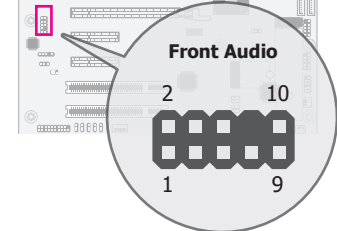
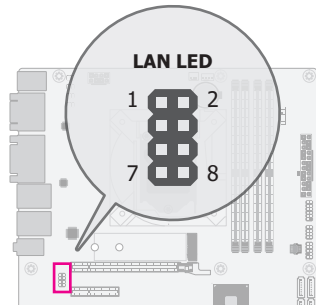
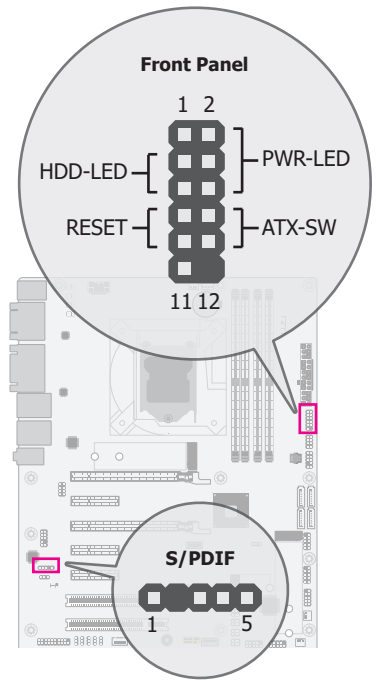
Pin	Assignment	Pin	Assignment
1	+5V	2	---
3	SPDIF Out	4	GND
5	SPDIF In		

## LAN LED

Pin	Assignment	Pin	Assignment
1	GBE_LED_1000-	2	GBE_LED_100-
3	GBE_LED_LINK_ACT-	4	3V3DU
5	LINK_1000_2	6	LINK_100_2
7	LINK_ACTIVITY_2	8	3V3DU

## Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	N.C.
5	Line2-R	6	Mic2-JD
7	Front I/O Sense	8	---
9	Line2-L	10	Line2-JD



## LPC

Pin	Assignment	Pin	Assignment
1	L_CLK	2	L_AD1
3	L_RST#	4	L_AD0
5	L_FRAME#	6	3V3
7	L_AD3	8	GND
9	L_AD2	10	---
11	INT_SERIRQ	12	GND
13	5VSB	14	5V

## PS2

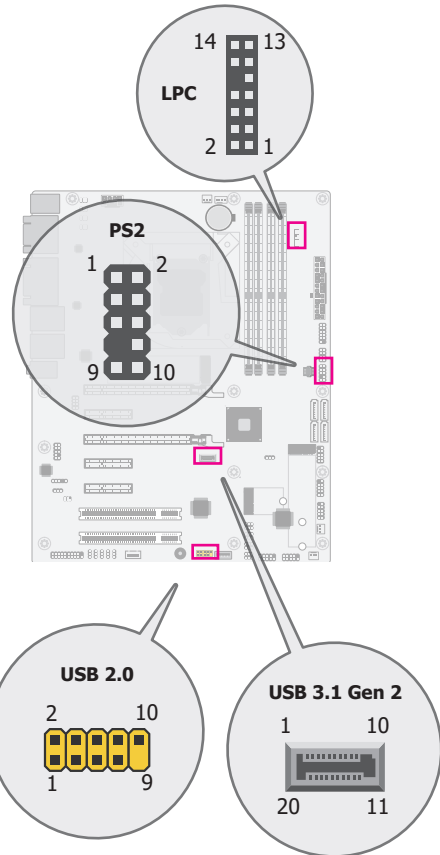
Pin	Function	Pin	Function
1	CLK	2	CLK
3	DAT	4	DAT
5	GND	6	GND
7	---	8	GND
9	BVCC	10	DIO12

## USB 3.1 Gen 2 Headers (USB 5/6)

Pin	Assignment	Pin	Assignment
1	GND	11	GND
2	USB3_TX5+	12	USB3_TX6-
3	USB3_TX5-	13	USB3_TX6+
4	GND	14	GND
5	USB3_RX5+	15	USB3_RX6-
6	USB3_RX5-	16	USB3_RX6+
7	GND	17	GND
8	D11+	18	D12+
9	D11-	19	D12-
10	SBV6	20	SBV6

## USB 2.0 Headers (USB 11/12)

Pin	Assignment	Pin	Assignment
1	VCC	2	VCC
3	Data-	4	Data-
5	Data+	6	Data+
7	GND	8	GND
9	---	10	N.C.



DFI reserves the right to change the specifications at any time prior to the product's release. For the latest information about the product, please visit the document download page at [go.dfi.com/CS630-H310](http://go.dfi.com/CS630-H310), [go.dfi.com/CS630-Q370](http://go.dfi.com/CS630-Q370), or via the QR codes to the right.



CS630-H310



CS630-Q370