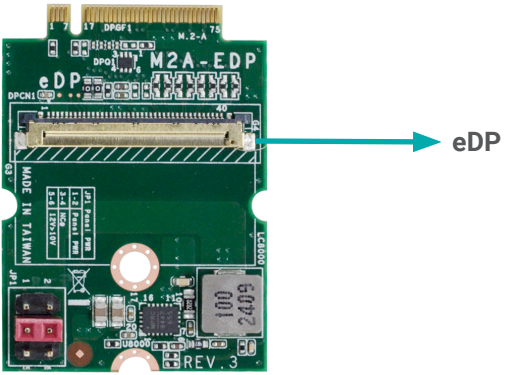
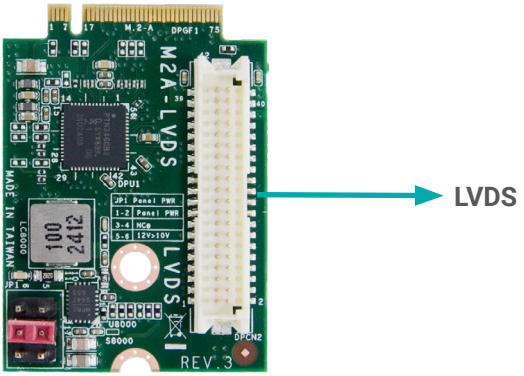
M2A-eDP/LVDS

eDP - Board Top View



LVDS - Board Top View



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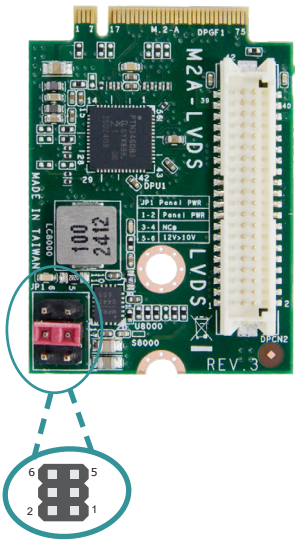
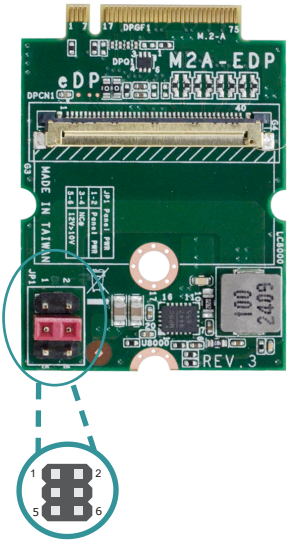
Jumper Setting

▶ Panel Power (JP1)





Panel Power	JP1
Panel Power	1-2 Short
NC	3-4 Short
12V > 10V	5-6 Short

PIN Assignment

▶ eDP

Pin	Function	Pin	Function
1	N.C	21	eDP_PANEL_PWR
2	N.C	22	eDP_PANEL_PWR
3	N.C	23	eDP_PANEL_PWR
4	N.C	24	GND
5	N.C	25	eDP_AUXN
6	N.C.	26	eDP_AUXP
7	N.C.	27	GND
8	N.C	28	eDP_LANE0_C_P
9	N.C	29	eDP_LANE0_C_N
10	eDP_GND	30	GND
11	eDP_GND	31	eDP_LANE1_C_P
12	eDP_GND	32	eDP_LANE1_C_N
13	eDP_GND	33	GND
14	eDP_HPD_CON	34	eDP_LANE2_C_P
15	GND	35	eDP_LANE2_C_N
16	GND	36	GND
17	GND	37	eDP_LANE3_C_P
18	GND	38	eDP_LANE3_C_N
19	N.C.	39	GND
20	eDP_PANEL_PWR	40	N.C.

▶ LVDS

Pin	Function	Pin	Function
1	GND	2	GND
3	LVDSA_LANE3_P	4	LVDSB_LANE3_P
5	LVDSA_LANE3_N	6	LVDSB_LANE3_N
7	GND	8	GND
9	LVDSA_LANE2_P	10	LVDSB_LANE2_P
11	LVDSA_LANE2_N	12	LVDSB_LANE2_N
13	GND	14	GND
15	LVDSA_LANE1_P	16	LVDSB_LANE1_P
17	LVDSA_LANE1_N	18	LVDSB_LANE1_N
19	GND	20	GND
21	LVDSA_LANE0_P	22	LVDSB_LANE0_P
23	LVDSA_LANE0_N	24	LVDSB_LANE0_N
25	GND	26	GND
27	LVDSA_CLK_P	28	LVDSB_CLK_P
29	LVDSA_CLK_N	30	LVDSB_CLK_N
31	GND	32	GND
33	LVDS_DDC_CLK	34	N.C.
35	LVDS_DDC_DATA	36	LVDS_3V3
37	LVDS_PANEL_PWR	38	LVDS_PANEL_PWR
39	LVDS_PANEL_PWR	40	LVDS_PANEL_PWR

▶ Mainboard Panel Control & Power

Pin	Function
1	GND
2	GND
3	DIMMING
4	VCC_PANEL_PWR
5	LVDS_3V3
6	BLONOFF
7	INV_PWR
8	INV_PWR

Note:
For Mainboard Panel Control connector location, please refer to the main board Quick Reference.

DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.