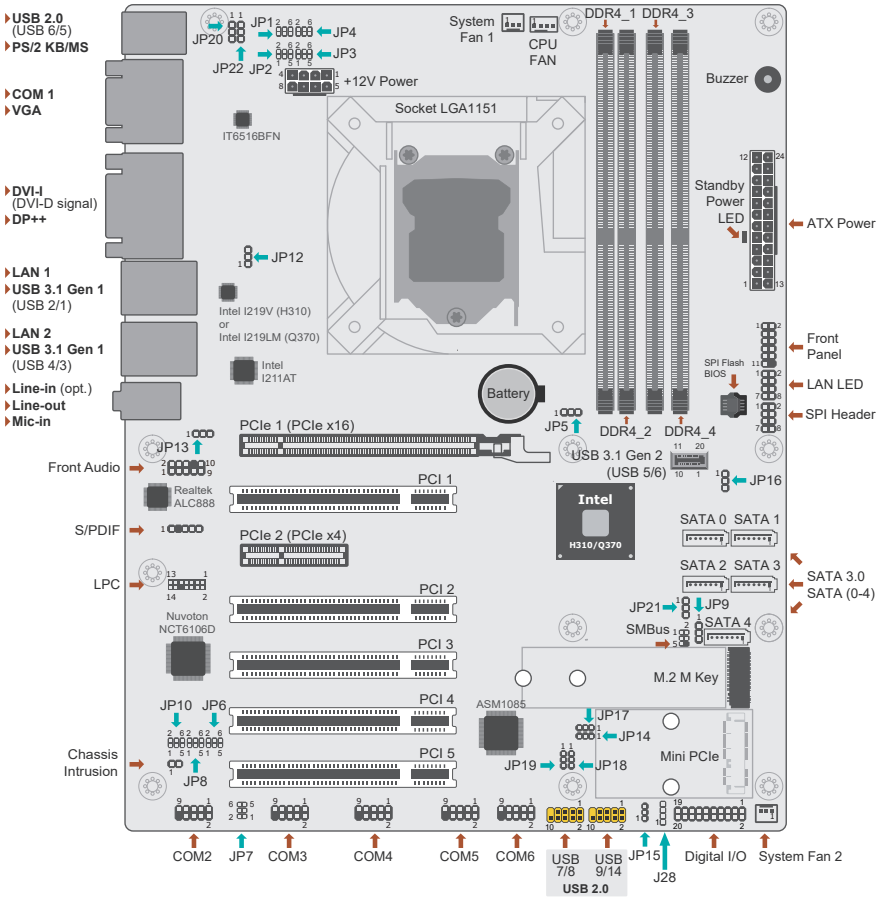


## Board Layout

### Top View





# Jumper Settings

<b>DIO Power Select (5V_DIO)</b>	<b>NA (Resistance control)</b>
5VDU (default)	1-2 On
5V	2-3 On

<b>AT/ATX Mode Select</b>	<b>NA (Resistance control)</b>
AT mode (default)	1-2 ON
ATX mode	2-3 ON

<b>PS/2</b>	<b>JP22</b>
+5VDU (default)	1-2 On
+5V	2-3 On

<b>USB Wake Up Select for SBV1</b>	<b>JP12</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>USB Wake Up Select for SBV2</b>	<b>JP13</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>USB Wake Up Select for SBV3</b>	<b>JP14</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>USB Wake Up Select for SBV4</b>	<b>JP20</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>USB Wake Up Select for SBV5</b>	<b>JP15</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>USB Wake Up Select for SBV6</b>	<b>JP16</b>
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

<b>DIO0~7 Power Select</b>	<b>JP18</b>
5VDU_DIO (default)	1-2 On
GND	2-3 On

<b>DIO8~15 Power Select</b>	<b>JP19</b>
5VDU_DIO (default)	1-2 On
GND	2-3 On

<b>Mini_PCIE Power Select</b>	<b>JP21</b>
3V3 (default)	1-2 On
3V3DU	2-3 On

<b>Mini_PCIE/mSATA</b>	<b>JP9</b>
Mini_PCIE	1-2 On
GND	2-3 On

<b>Selection for COM1</b>	<b>JP1</b>
RS232 (default)	1-2 ON
RS422 Duplex	3-4 ON
RS485	5-6 ON

<b>Selection for COM1/RS485</b>	<b>JP2 &amp; JP3</b>
RS232 (default)	1-3, 2-4 ON
RS485	3-5, 4-6 ON

<b>Selection for COM2</b>	<b>JP10</b>
RS232 (default)	1-2 ON
RS422 Duplex	3-4 ON
RS485	5-6 ON

<b>Selection for COM2/RS485</b>	<b>JP6 &amp; JP8</b>
RS232 (default)	1-3, 2-4 ON
RS485	3-5, 4-6 ON

## ▶ System Fan1 (J7)

Pin	Assignment
1	GND
2	PWM
3	TACH

## ▶ System Fan2 (J22)

Pin	Assignment
1	GND
2	PWM
3	TACH

## ▶ CPU Fan (J8)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

## ▶ DIO Power (J28)

Pin	Assignment
1	12V
2	GND
3	5VDU
4	5V

## ▶ Front LAN LED (J19)

Pin	Assignment
1	GBE_LED_1000-
2	GBE_LED_1000-
3	GBE_LED_LINK_ACT-
4	3V3DU
5	LINK_1000_
6	LINK_100
7	LINK_ACTIVITY
8	3V3DU

## ▶ SMBus Header (J12)

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK_RESUME	4	SMB_DATA_RESUME
5	SMBALERT_PCH-		

### ▶ SATA0 (J23)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### ▶ SATA1 (J24)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### ▶ SATA2 (J17)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### ▶ SATA3 (J25)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### ▶ SATA4 (J27)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

### ▶ SPI Header (J20)

Pin	Assignment	Pin	Assignment
1	V_3P3_SPI	2	GND
3	ICHSPI_CS0-	4	SPI_CLK-R
5	SPI_SO-R	6	SPI_SI-R
7	HOLD		

### ► COM1 (CN5)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MRD_CONN
3	MTD_CONN	4	MDTR-_CONN
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### ► COM2 (J9)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MRD_CONN
3	MTD_CONN	4	MDTR-_CONN
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### ► COM3 (J11)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### ► COM4 (J13)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### ► COM5 (J15)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### ► COM6 (J16)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

## ► Front Panel (J18)

Pin	Assignment	Pin	Assignment
1	NC	2	V_SUS_LED
3	3V3	4	V_SUS_LED
5	HDD_LED	6	SUS_LED
7	GND	8	GND
9	SYS_RST-	10	PWR_BTN-
11	NC	12	---

## ► DIO (J26)

Pin	Assignment	Pin	Assignment
1	GND	11	DIO8
2	GND	12	DIO9
3	DIO7	13	DIO10
4	DIO6	14	DIO11
5	DIO5	15	DIO12
6	DIO4	16	DIO13
7	DIO3	17	DIO14
8	DIO2	18	DIO15
9	DIO1	19	GND
10	DIO0	20	NC



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

