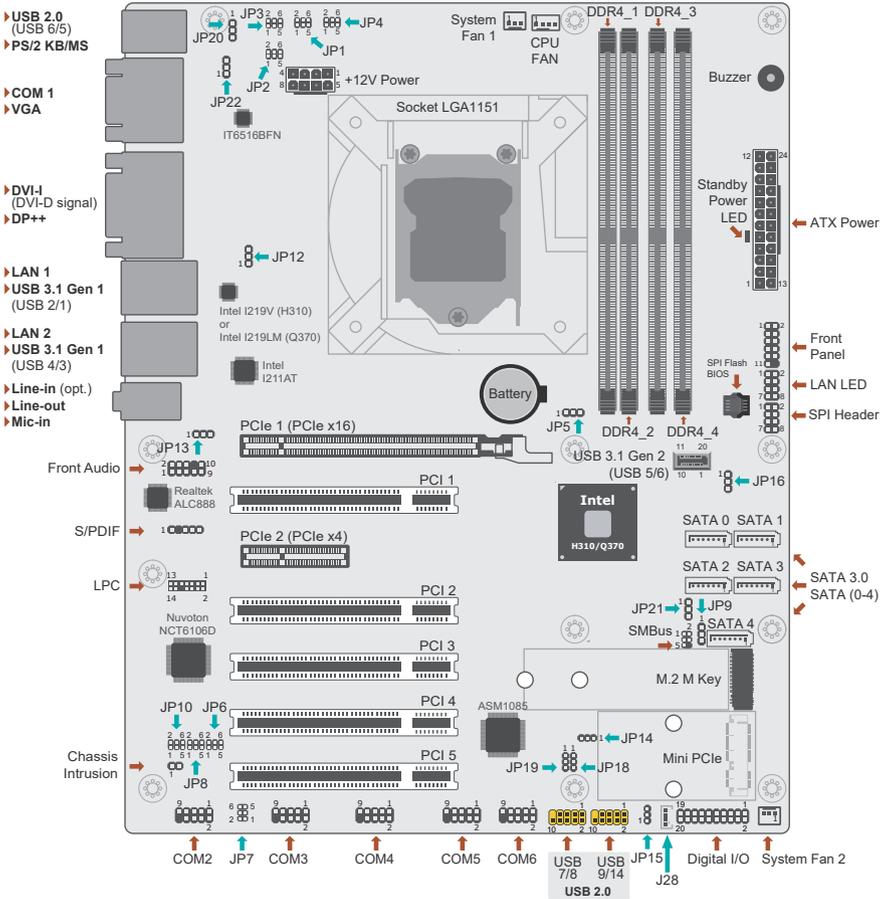


Board Layout

Top View





Jumper Settings

COM1 Power Selection	JP4
RS232 (default)	1-3, 2-4 ON
RS232 with Power	3-5, 4-6 ON

COM2 Power Selection	JP7
RS232 (default)	1-3, 2-4 ON
RS232 with Power	3-5, 4-6 ON

PS/2	JP22
+5VDU (default)	1-2 On
+5V	2-3 On

USB Wake Up Select for SBV1	JP12
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

USB Wake Up Select for SBV2	JP13
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

USB Wake Up Select for SBV3	JP14
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

USB Wake Up Select for SBV4	JP20
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

USB Wake Up Select for SBV5	JP15
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

USB Wake Up Select for SBV6	JP16
Control by BIOS (default)	1-2 On
Wake up disable	2-3 On

DIO0~7 Power Select	JP18
5VDU_DIO (default)	1-2 On
GND	2-3 On

DIO8~15 Power Select	JP19
5VDU_DIO (default)	1-2 On
GND	2-3 On

Mini_PCIE Power Select	JP21
3V3 (default)	1-2 On
3V3DU	2-3 On

Mini_PCIE/mSATA	JP9
Mini_PCIE	1-2 On
mSATA	2-3 On

Selection for COM1	JP1
RS232 (default)	1-2 ON
RS422 Duplex	3-4 ON
RS485	5-6 ON

Selection for COM1/RS485	JP2 & JP3
RS232 (default)	1-3, 2-4 ON
RS485	3-5, 4-6 ON

Selection for COM2	JP10
RS232 (default)	1-2 ON
RS422 Duplex	3-4 ON
RS485	5-6 ON

Selection for COM2/RS485	JP6 & JP8
RS232 (default)	1-3, 2-4 ON
RS485	3-5, 4-6 ON

▶ System Fan1 (J7)

Pin	Assignment
1	GND
2	PWM
3	TACH

▶ System Fan2 (J22)

Pin	Assignment
1	GND
2	PWM
3	TACH

▶ CPU Fan (J8)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

▶ DIO Power (J28)

Pin	Assignment
1	12V
2	GND
3	5VDU
4	5V

▶ Front LAN LED (J19)

Pin	Assignment
1	GBE_LED_1000-
2	GBE_LED_100-
3	GBE_LED_LINK_ACT-
4	3V3DU
5	LINK_1000_
6	LINK_100
7	LINK_ACTIVITY
8	3V3DU

▶ SMBus Header (J12)

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK_RESUME	4	SMB_DATA_RESUME
5	SMBALERT_PCH-		

► SATA0 (J23)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

► SATA1 (J24)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

► SATA2 (J17)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

► SATA3 (J25)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

► SATA4 (J27)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

► SPI Header (J20)

Pin	Assignment	Pin	Assignment
1	V_3P3_SPI	2	GND
3	ICHSPI_CS0-	4	SPI_CLK-R
5	SPI_SO-R	6	SPI_SI-R
7	HOLD		

► COM1 (CN5)

Pin	Assignment	Pin	Assignment
1	MDCD1-/RS422_RX+ / RS485_D+	2	MSIN1-/RS422_RX- / RS485_D-
3	MSO1-/RS422_TX+	4	MDTR1-/RS422_TX-
5	GND	6	MDSR1-
7	MRTS1-	8	MCTS1-
9	MRI1-	10	--

► COM2 (J9)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MRD_CONN
3	MTD_CONN	4	MDTR-_CONN
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

► COM3 (J11)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

► COM4 (J13)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

► COM5 (J15)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

► COM6 (J16)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

► Front Panel (J18)

Pin	Assignment	Pin	Assignment
1	NC	2	V_SUS_LED
3	3V3	4	V_SUS_LED
5	HDD_LED	6	SUS_LED
7	GND	8	GND
9	SYS_RST-	10	PWR_BTN-
11	NC	12	---

► DIO (J26)

Pin	Assignment	Pin	Assignment
1	GND	11	DIO8
2	GND	12	DIO9
3	DIO7	13	DIO10
4	DIO6	14	DIO11
5	DIO5	15	DIO12
6	DIO4	16	DIO13
7	DIO3	17	DIO14
8	DIO2	18	DIO15
9	DIO1	19	GND
10	DIO0	20	NC



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

