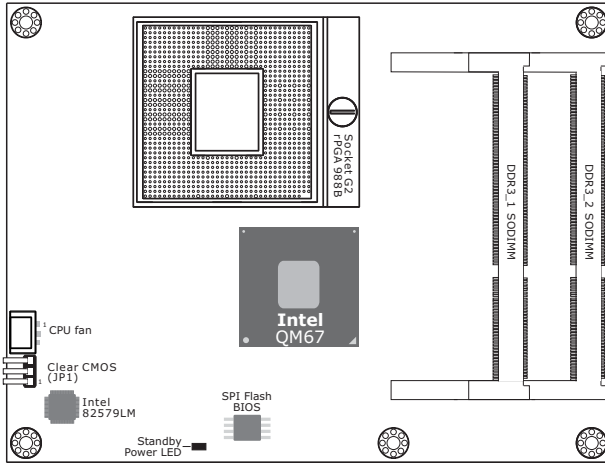


DFI®

HR900-B



TOP



BOTTOM

Clear CMOS Data	JP1
Normal (default)	1-2 On
Clear CMOS Data	2-3 On