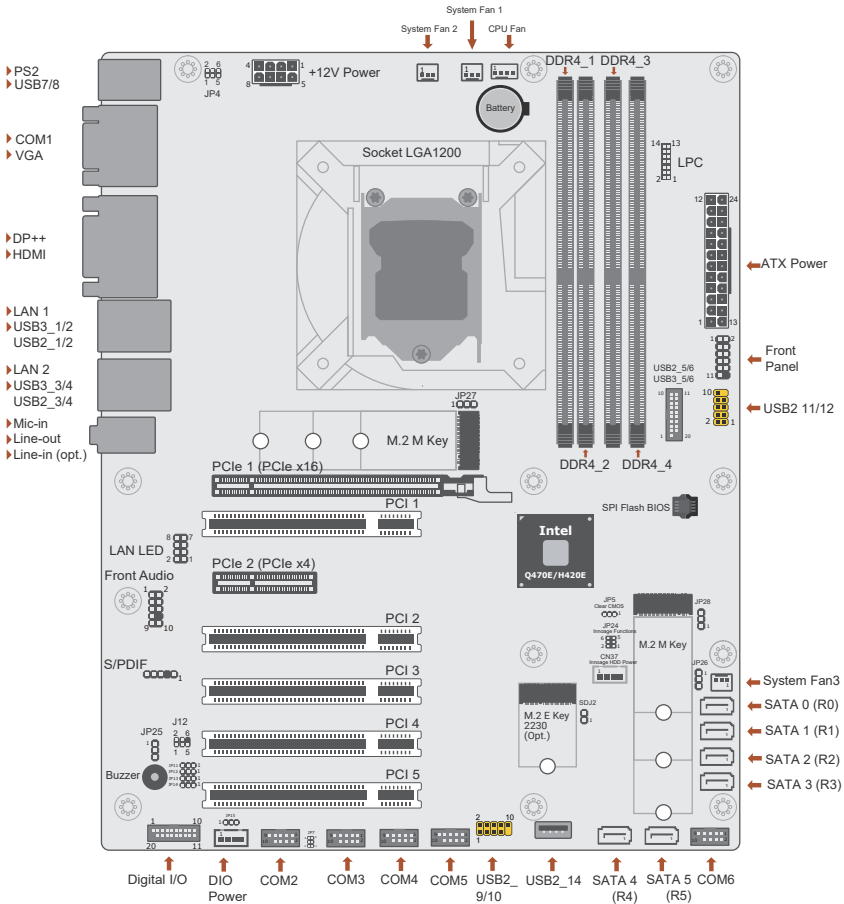


Board Layout

Top View





Jumper Settings

COM1 / COM2 RS232 Power	JP4/7
RS232 (default)	1-3, 2-4 On
RS232 with power	3-5, 4-6 On

Clear CMOS Data	JP5
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

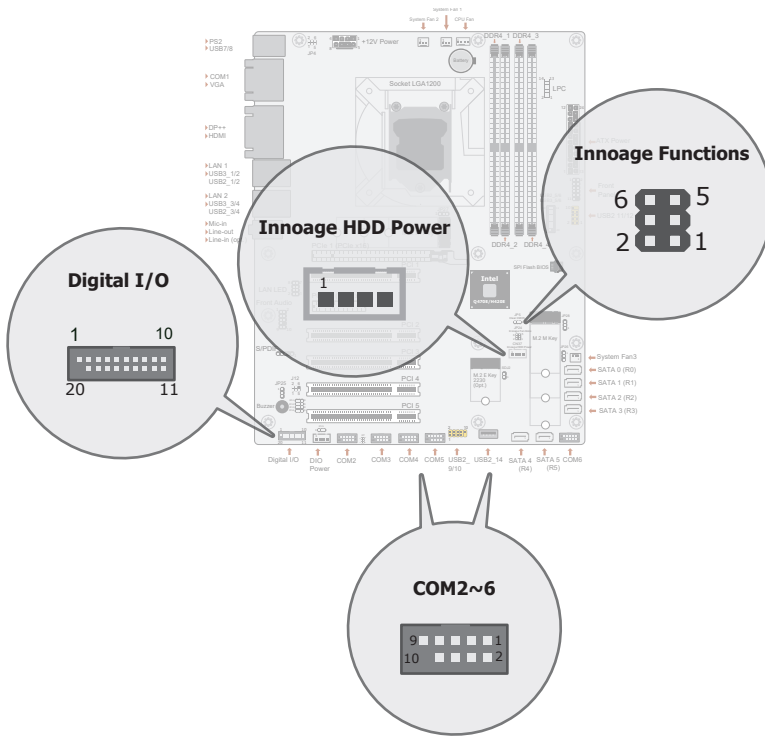
Digital I/O Output	JP11 DIO 0-3 PWR JP12 DIO 4-7 PWR JP13 DIO 8-11 PWR JP14 DIO 12-15 PWR
DIO PWR (default)	1-2 On
GND	2-3 On

Digital I/O Power select	JP15
+5VSB (default)	1-2 On
+5V	2-3 On

AT / ATX Mode	JP26
AT: GND (default)	1-2 On
ATX: 3V3SB	2-3 On

M.2 Power Control	JP27/JP28
3V3 (default)	1-2 On
3V3DU	2-3 On

PIN Assignment



► Innoage Functions (JP24)

Pin	Assignment
1	INNOAGE_PCRST#
2	INNOAGE_RECOV#
3	INNOAGE_PWRLED#
4	INNOAGE_HDDLED#
5	INNOAGE_PWRBTN#
6	GND

► Innoage HDD Power (CN37)

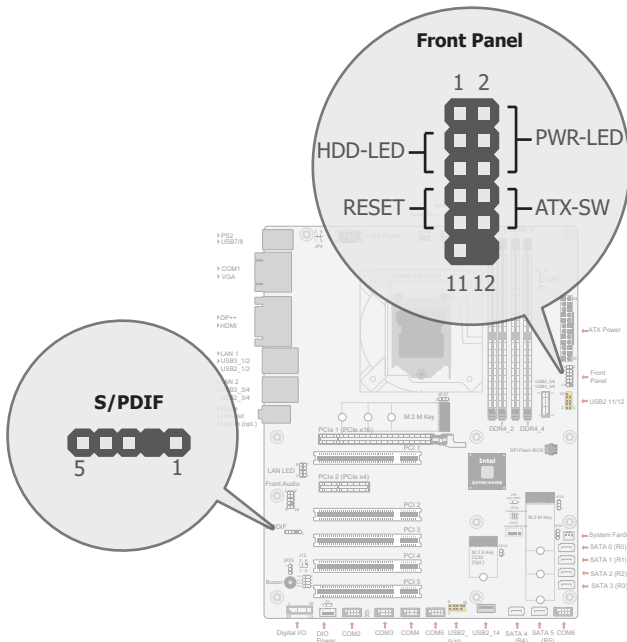
Pin	Assignment
1	+12V_SATA
2	GND
3	GND
4	5V_SATA

► COM Ports 2~6 (J9, J11, J13, J15, J16)

Pin	RS232	RS232 with Power	RS422 Full Duplex	RS485
1	DCD-	+12V	RX+	Data+
2	SIN-	SIN-	RX-	Data-
3	SO-	SO-	TX+	---
4	DTR-	DTR-	TX-	---
5	GND	GND	GND	GND
6	DSR-	DSR-	---	---
7	RTS-	RTS-	---	---
8	CTS-	CTS-	---	---
9	RI-	+5V	---	---

► Digital I/O (J2)

Pin	Function	Pin	Function
1	GND	2	GND
3	DIO7	4	DIO6
5	DIO5	6	DIO4
7	DIO3	8	DIO2
9	DIO1	10	DIO0
11	DIO8	12	DIO9
13	DIO10	14	DIO11
15	DIO12	16	DIO13
17	DIO14	18	DIO15
19	GND	20	---

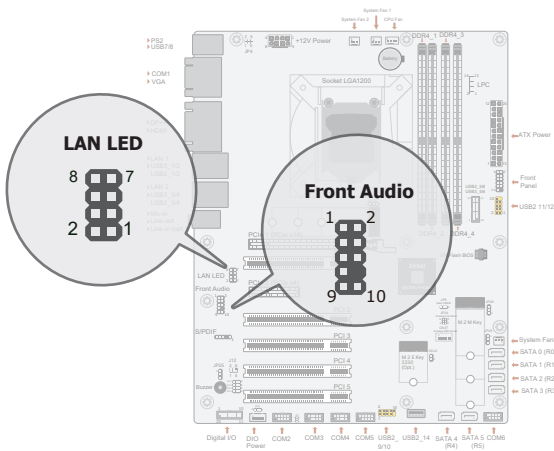


► S/PDIF (AUJ1)

Pin	Assignment
1	+5V
2	-
3	SPDIF Out
4	GND
5	SPDIF In

► Front Panel (J18)

Pin	Assignment	Pin	Assignment
1	N.C.	9	PM_SYSRST
2	3V3SB	10	PWR_BTN-
3	3V3	11	N.C.
4	3V3SB	12	--
5	HD_LED		
6	SUS_LED		
7	Ground		
8	Ground		

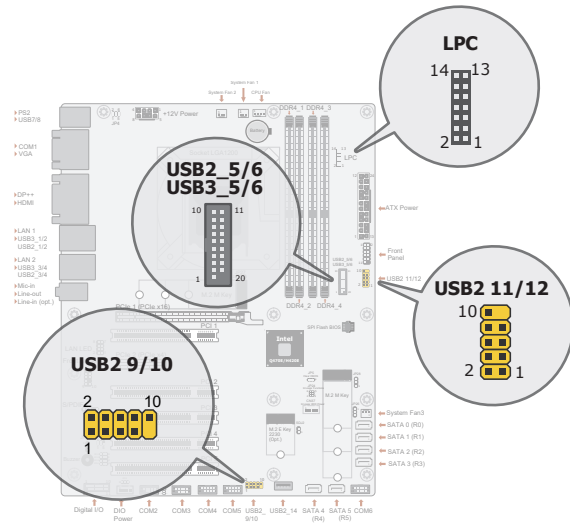


► LAN LED (ETJ2)

Pin	Assignment	Pin	Assignment
1	GBE_LED_1000-	2	GBE_LED_100-
3	GBE_LED_LINK_ACT-	4	3V3DU
5	LINK_1000_2	6	LINK_100_2
7	LINK_ACTIVITY_2	8	3V3DU

► Front Audio (AUJ2)

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	N.C.
5	Line2-R	6	Mic2-JD
7	Front I/O Sense	8	---
9	Line2-L	10	Line2-JD



► LPC (J21)

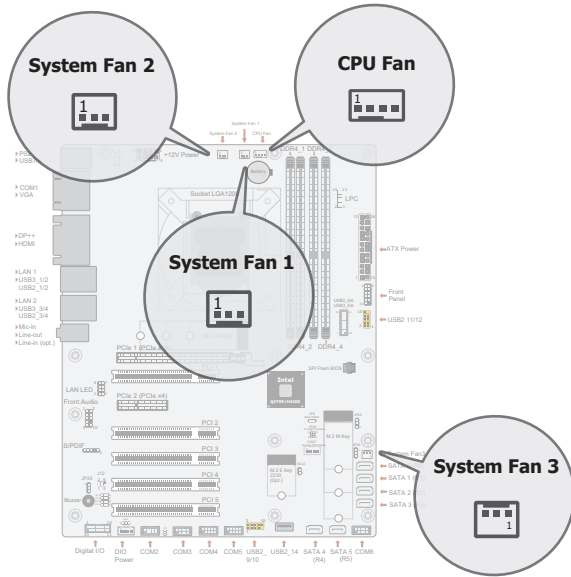
Pin	Assignment	Pin	Assignment
1	CLK	2	L_AD1
3	RST#	4	L_AD0
5	FRAME#	6	VCC3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5V5B	14	5V

► **USB 2.0 9/10 & 11/12 (UBJ4 & UBJ1)**

Pin	Assignment	Pin	Assignment
1	SBV	2	SBV
3	Data-	4	Data-
5	Data+	6	Data+
7	GND	8	GND
9	---	10	N.C.

► **USB 2.0/3.1 5/6 (UBJ3)**

Pin	Assignment	Pin	Assignment
10	5V	11	USBP_C_5P
9	USBP_C_6P	12	USBP_C_5N
8	USBP_C_6N	13	GND
7	GND	14	USB3_TX6_DP
6	USB3_TX5_DP	15	USB3_TX6_DN
5	USB3_TX5_DN	16	GND
4	GND	17	USB3_RX6_DP
3	USB3_RX5_DP	18	USB3_RX6_DN
2	USB3_RX5_DN	19	SBV3
1	SBV3	20	---



▶ **System Fan 1 (J7)**

▶ **System Fan 2 (J22)**

▶ **System Fan 3 (J30)**

▶ **CPU Fan (J8)**

Pin	Assignment
1	GND
2	PWM
3	TACH

Pin	Assignment
1	Ground
2	12V
3	RPM
4	Control



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

