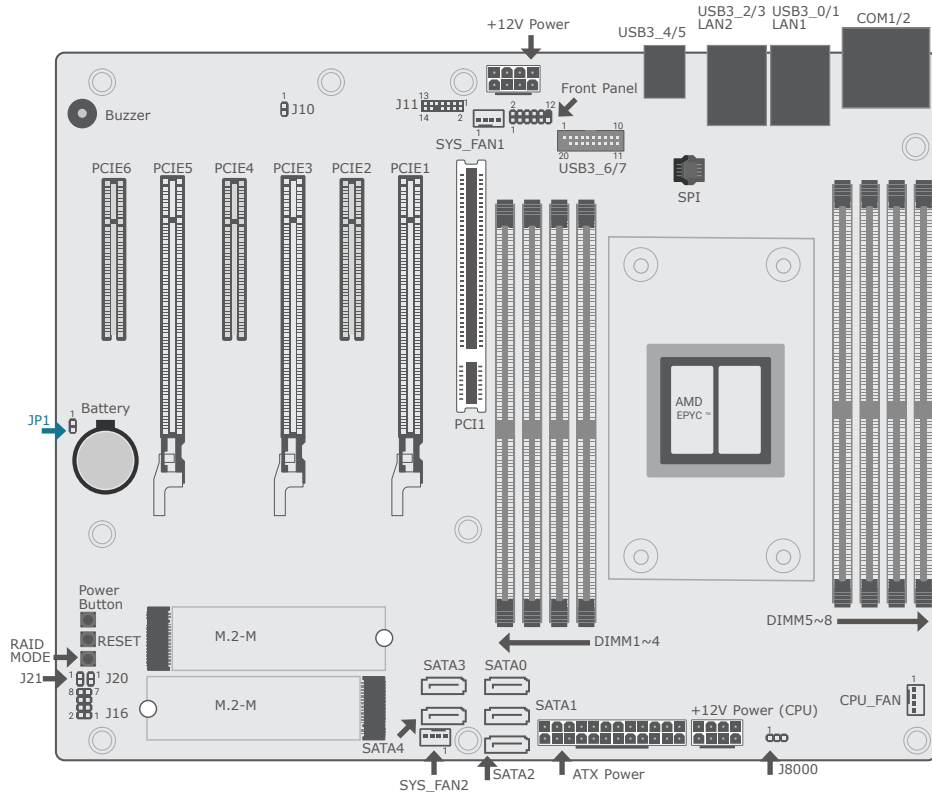


Board Layout and Jumper Settings



Clear CMOS Data	JP1
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

Pin Assignments for I/O Connectors

Front Panel

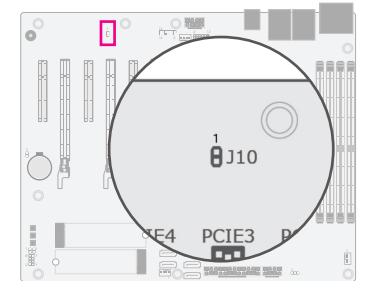
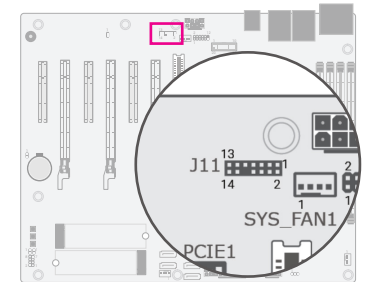
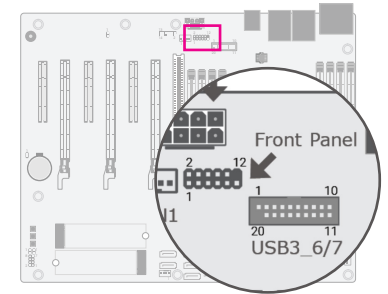
Pin	Assignment	Pin	Assignment
1	N.C.	2	LED Power
3	3V3	4	LED Power
5	Signal	6	Signal
7	Ground	8	Ground
9	Signal	10	Signal
11	N.C.	12	---

J11 Debug

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	FRAME#	6	VCC3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5VSB	14	5V

J10 Chassis Intrusion

Pin	Assignment	Pin	Assignment
1	Signal	2	GND



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S0630