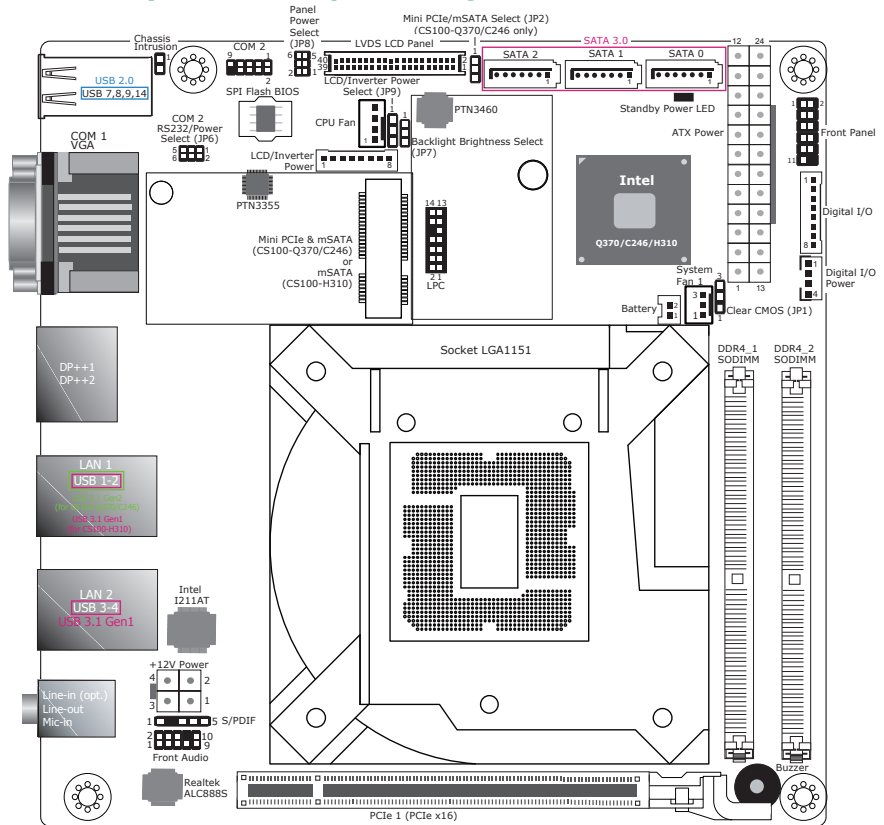
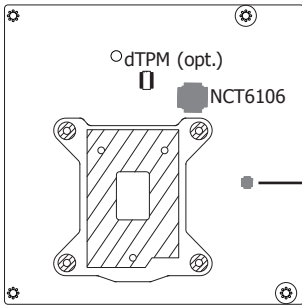


Board Layout and Jumper Settings



TOP

Backlight Brightness Select	JP7	Clear CMOS	JP1
+3.3V (default)	1-2 On	Normal (default)	1-2 On
+5V	2-3 On	Clear CMOS	2-3 On
LCD/Inverter Power Select	JP9	COM 2 RS232/Power Select	JP6
+12V (default)	1-2 On	RS232 (default)	1-3 (RI-), 2-4 (DCD-) On
+5V	2-3 On	RS232 with power	3-5 (+5V), 4-6 (+12V) On
Panel Power Select	JP8	Mini PCIe/mSATA Signal Select (CS100-Q370/C246 only)	JP2
+12V	1-2 On	Mini PCIe (default)	1-2 On
+5V	3-4 On	mSATA	2-3 On
+3.3V (default)	5-6 On		



Intel I219LM (for CS100-Q370/C246)
or I219V (for CS100-H310)

BOTTOM



Pin Assignments

Front Panel

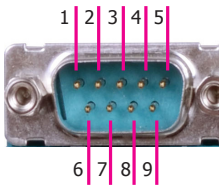
	Pin	Assignment		Pin	Assignment
	1	NC	PWR-LED	2	LED Power
HD-LED	3	HDD Power		4	LED Power
	5	Signal		6	Signal
RESET	7	GND	ATX-SW	8	GND
	9	Signal		10	Signal
	11	NC		12	---

SATA 3.0 0/1/2

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

COM 1 Port: RS232

COM 2 Header: RS232/RS232 with Power



COM 1

Pin	RS232	RS232 with Power
1	DCD-	+12V
2	SIN-	SIN-
3	SO-	SO-
4	DTR-	DTR-
5	GND	GND
6	DSR-	DSR-
7	RTS-	RTS-
8	CTS-	CTS-
9	RI-	+5V

+12V Power

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	+12V	4	+12V

ATX Power

Pin	Assignment	Pin	Assignment
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON#
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PWR_OK	20	NC
9	+5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND

LPC

Pin	Assignment	Pin	Assignment
1	L_CLK	2	L_AD1
3	L_RST#	4	L_AD0
5	L_FRAME#	6	3V3
7	L_AD3	8	GND
9	L_AD2	10	---
11	INT_SERIRQ	12	GND
13	5VDU	14	5V

LVDS

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDSA_Out3+	4	LVDSB_Out3+
5	LVDSA_Out3-	6	LVDSB_Out3-
7	GND	8	GND
9	LVDSA_Out2+	10	LVDSB_Out2+
11	LVDSA_Out2-	12	LVDSB_Out2-
13	GND	14	GND
15	LVDSA_Out1+	16	LVDSB_Out1+
17	LVDSA_Out1-	18	LVDSB_Out1-
19	GND	20	GND
21	LVDSA_Out0+	22	LVDSB_Out0+
23	LVDSA_Out0-	24	LVDSB_Out0-
25	GND	26	GND
27	LVDSA_CLK+	28	LVDSB_CLK+
29	LVDSA_CLK-	30	LVDSB_CLK-
31	GND	32	GND
33	DDC_CLK	34	NC
35	DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power

LVDS/Inverter Power

Pin	Assignment
1	GND
2	GND
3	Panel Inverter Brightness Voltage Control
4	Panel Power
5	+3.3V
6	Panel Backlight On/Off Control
7	LCD/Inverter Power
8	LCD/Inverter Power

Digital I/O

Pin	Assignment
1	DIO7
2	DIO6
3	DIO5
4	DIO4
5	DIO3
6	DIO2
7	DIO1
8	DIO0

Digital I/O Power

Pin	Assignment
1	+12V
2	GND
3	5VDU
4	+5V

Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	NC
5	Line2-R	6	Mic2-JD
7	GND	8	---
9	Line2-L	10	Line2-JD

S/PDIF

Pin	Assignment
1	+5V
2	---
3	SPOUT
4	GND
5	SPIN

Battery

Pin	Assignment
1	+3.3V
2	GND

CPU Fan

Pin	Assignment
1	GND
2	12V
3	Sense
4	Speed Control

System Fan 1

Pin	Assignment
1	GND
2	PWR
3	Sense

Chassis Intrusion

Pin	Assignment
1	Signal
2	GND



DFI reserves the right to change the specifications at any time prior to the product's release. For the latest information about the product, please refer to the user's manual at go.dfi.com/CS100.

