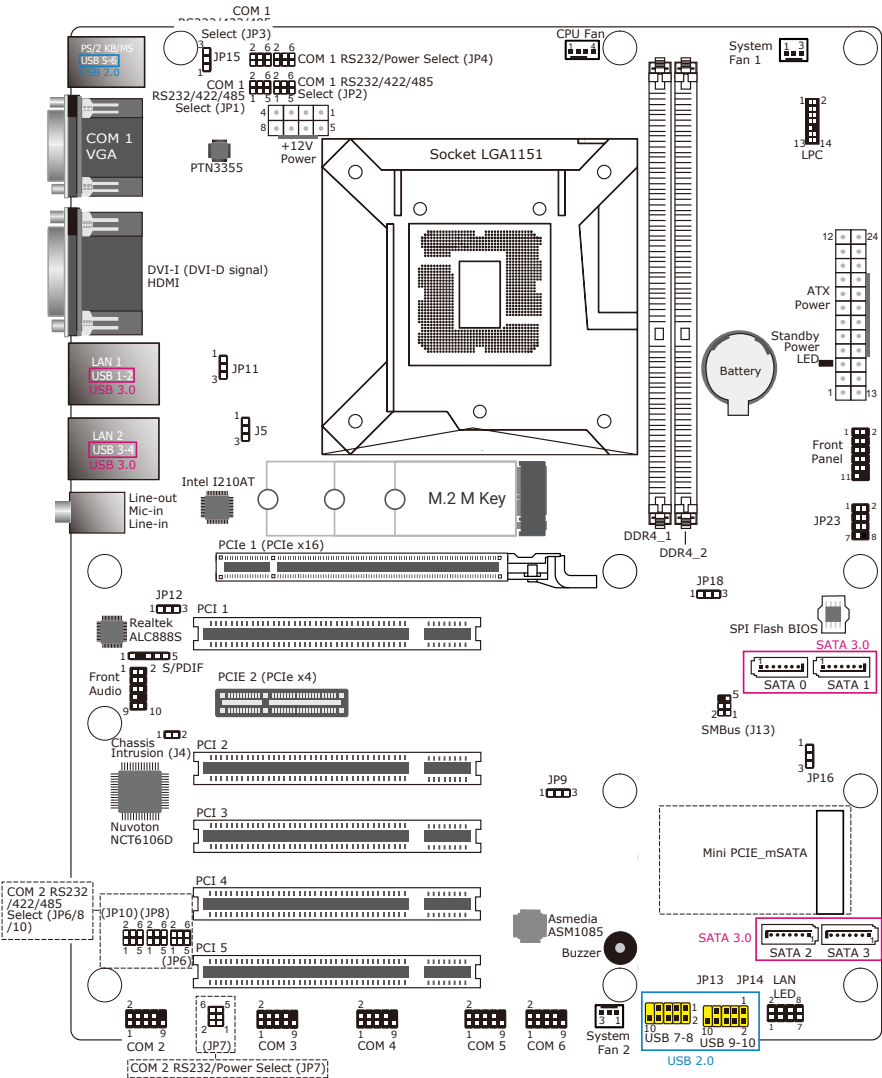


Board Layout



Notes:

1. When COM 1 RS232/422/485 is being configured, JP1, JP2, and JP3 must be set in accordance with the same serial mode.
2. When COM 2 RS232/422/485 is being configured, JP6, JP8, and JP10 must be set in accordance with the same serial mode.

Jumper Settings

RS232/422/485 Select	JP3 (COM 1) JP10 (COM 2)	JP1/JP2 (COM 1) JP6/JP8 (COM 2)
RS232 (default)	1-3, 4-6 On	1-3, 2-4 On
RS422 Full Duplex	3-5, 4-6 On	3-5, 4-6 On
RS485	3-5, 2-4 On	3-5, 4-6 On

RS232/Power Select	JP4 (COM 1), JP7 (COM 2)
RS232 (default)	1-3 (RI-), 2-4 (DCD-) On
RS232 with power	3-5 (+5V), 4-6 (+12V) On

Clear CMOS	JP9	USB2_5/6	JP15
Normal (default)	1-2 On	+5VDU (default)	1-2 On
Clear CMOS Data	2-3 On	+5V	2-3 On

USB3_1/2, USB3_3/4	JP11/JP12	USB2_7/8, USB2_9/10	JP13/JP14
+5VDU (default)	1-2 On	+5VDU (default)	1-2 On
+5V	2-3 On	+5V	2-3 On

SATA/mSATA Signal Select	JP16
SATA (default)	1-2 On
mSATA	2-3 On

PCIe/M.2 Signal Select	JP18
PCIe (default)	1-2 On
M.2	2-3 On

Pin Assignment

Digital I/O (DIO)

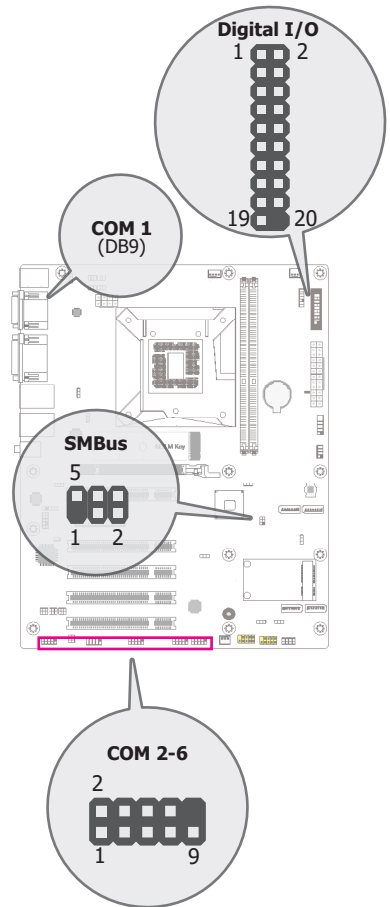
Pin	Function	Pin	Function
1	GND	2	+12V
3	DIO7	4	+12V
5	DIO6	6	GND
7	DIO5	8	+5V
9	DIO4	10	+5V
11	DIO3	12	GND
13	DIO2	14	+5V _{DU}
15	DIO1	16	+5V _{DU}
17	DIO0	18	GND
19	GND	20	---

COM Ports (COM 1-6)

Pin	RS232	RS232 with Power	RS422 Full Duplex	RS485
1	DCD-	+12V	RXD+	Data+
2	SIN-	SIN-	RXD-	Data-
3	SO-	SO-	TXD+	N.C.
4	DTR-	DTR-	TXD-	N.C.
5	GND	GND	GND	GND
6	DSR-	DSR-	N.C.	N.C.
7	RTS-	RTS-	N.C.	N.C.
8	CTS-	CTS-	N.C.	N.C.
9	RI-	+5V	N.C.	N.C.

SMBus

Pin	Assignment	Pin	Assignment
1	3V3 _{DU}	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_ALERT	6	---



Front Panel

Pin	Assignment	Pin	Assignment
1	N.C.	2	LED Power
3	HDD Power	4	LED Power
5	Signal	6	Signal
7	Ground	8	Ground
9	Signal	10	Signal
11	N.C.	12	---

S/PDIF

Pin	Assignment	Pin	Assignment
1	+5V	2	---
3	SPDIF Out	4	GND
5	SPDIF In		

LAN LED

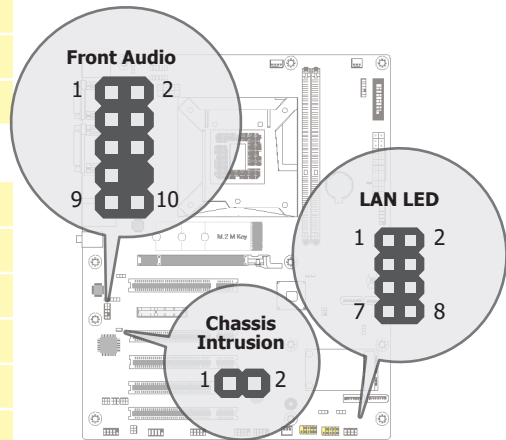
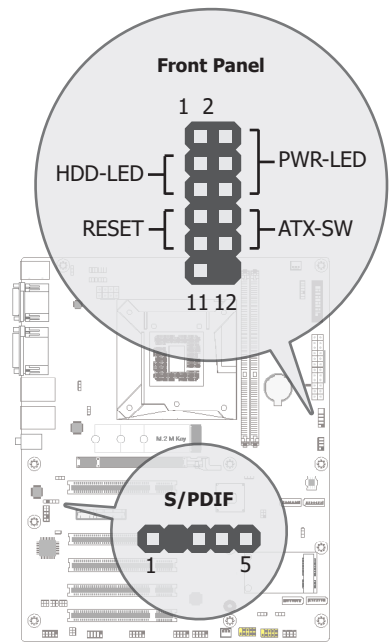
Pin	Assignment	Pin	Assignment
1	GBE_LED_1000-	2	GBE_LED_100-
3	GBE_LED_LINK_ACT-	4	3V3DU
5	LINK_1000_4	6	LINK_100_4
7	LINK_ACTIVITY_4	8	3V3DU

Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	N.C.
5	Line2-R	6	Mic2-JD
7	Front I/O Sense	8	---
9	Line2-L	10	Line2-JD

Chassis Intrusion - J4

Pin	Function	Pin	Function
1	Signal	2	GND



J5

Pin	Assignment	Pin	Assignment
1	3V3	2	Signal
3	GND		

USB 2.0 Headers (USB 7/8)

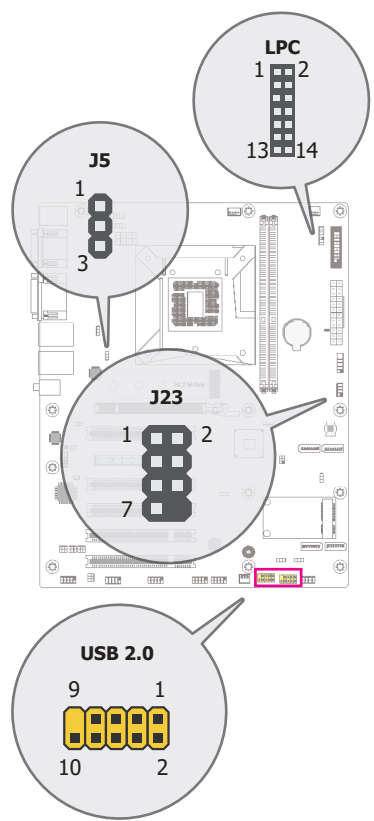
Pin	Assignment	Pin	Assignment
1	SBV3	2	SBV3
3	USBP_C_7N	4	USBP_C_8N
5	USBP_C_7P	6	USBP_C_8P
7	GND	8	GND
9	---	10	N.C.

USB 2.0 Headers (USB 9/10)

Pin	Assignment	Pin	Assignment
1	SBV5	2	SBV5
3	USBP_C_9N	4	USBP_C_10N
5	USBP_C_9P	6	USBP_C_10P
7	GND	8	GND
9	---	10	N.C.

LPC

Pin	Assignment	Pin	Assignment
1	CLK	2	L_AD1
3	RST#	4	L_AD0
5	FRAME#	6	VCC3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5VSB	14	5V



SPI PROGRAM HEADER (OPTIONAL) - J23

Pin	Assignment	Pin	Assignment
1	V_3P3_SPI	2	GND
3	ICHSPI_CS0-	4	SPI_CLK-R
5	SPI_SO-R	6	SPI_SI-R
7	HOLD	8	---